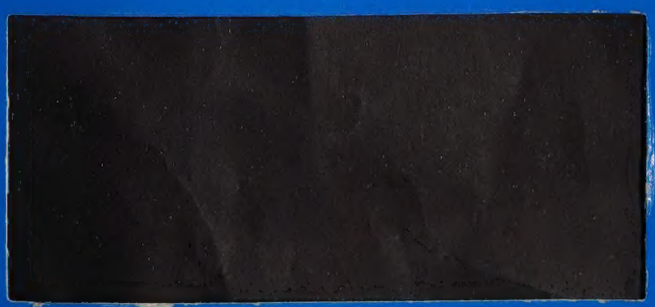
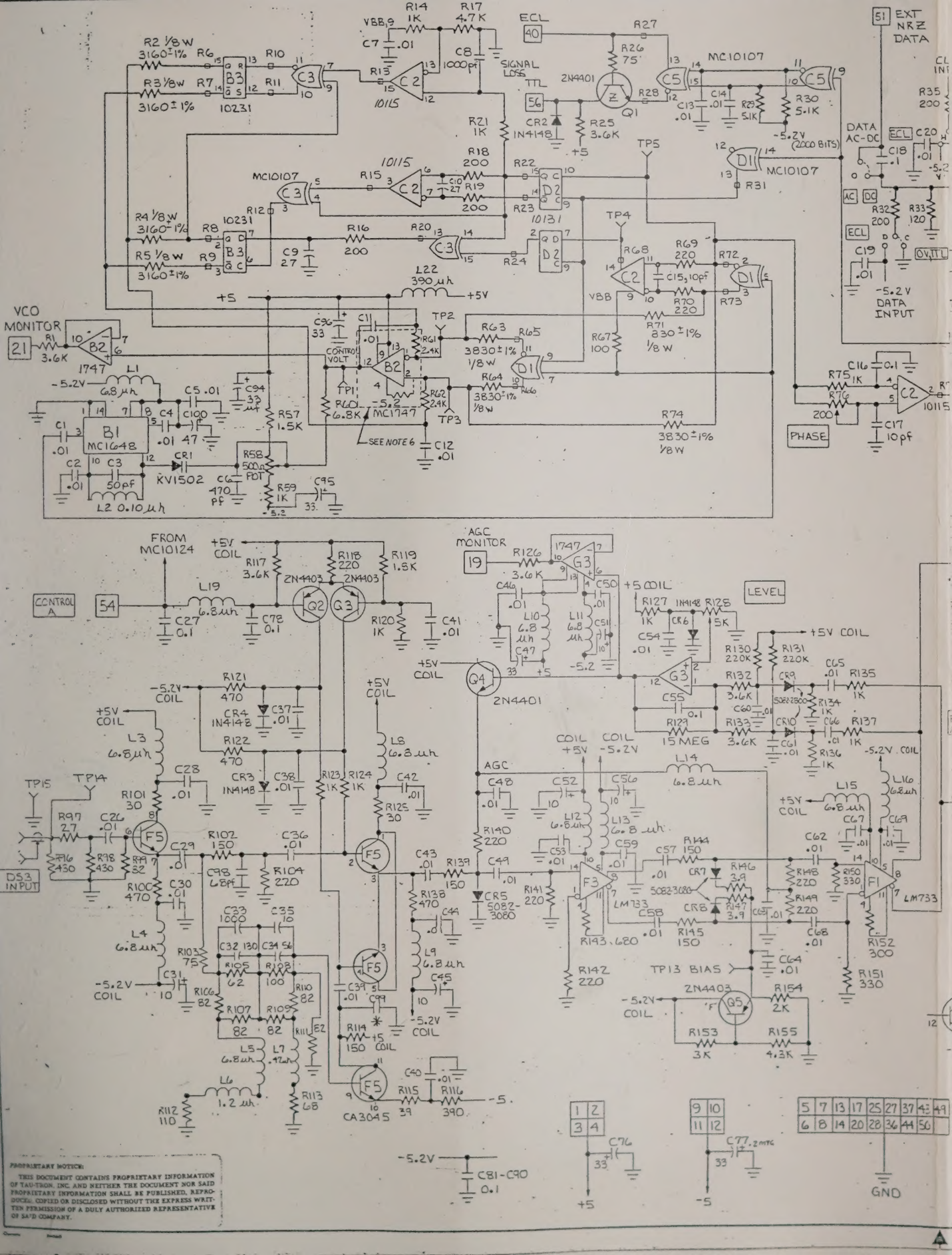


tau-tron

A UNIT OF
GENERAL SIGNAL

SCHEMATICS

5250


VCO MONITOR
21
1747
B1 MC1648
B2
B3
10231
C1
C2
C3
C4
C5
C6
C7
C8
C9
C10
C11
C12
C15, 10pf
C16
C17 10pf
C18
C19
C20
C26
C27
C28
C29
C30
C31
C32 130
C33 1000
C34 56
C35 10
C36
C37
C38
C40
C41
C42
C43
C44
C45
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C64
C65
C66
C67
C68
C69
C76
C77
C78
C80
C81-C90
C94
C95
C96
C98 68pf
C99
CR1 KV1502
CR2 IN4148
CR3 IN4148
CR4 IN4148
CR5 5082-3080
CR6
CR7 5082-3080
CR8
CR9 5082-2800
CR10
D1 MC10107
D2 10131
F1 LM733
F3 LM733
F5 CA3045
G3
Q1 2N4401
Q2 2N4403
Q3 2N4403
Q4 2N4401
Q5 2N4403
R1 3.6K
R2 1/8W 3160±1%
R3 1/8W 3160±1%
R4 1/8W 3160±1%
R5 1/8W 3160±1%
R6
R7
R8
R9
R10
R11
R12
R13
R14 1K
R15
R16 200
R17 4.7K
R18 200
R19 200
R20
R21 1K
R22
R23
R24
R25 3.6K
R26 75
R27
R28
R29 5.1K
R30 5.1K
R31
R32 200
R33 120
R57 1.5K
R58 500Ω POT
R59 1K
R62 2.4K
R63 3830±1% 1/8W
R64 3830±1% 1/8W
R65
R66
R67 100
R68
R69 220
R70 220
R71 830±1% 1/8W
R72
R73
R74 3830±1% 1/8W
R75 1K
R76 200
R97 27
R98 430
R99 82
R100 470
R101 30
R102 150
R103 75
R104 220
R105 62
R106 82
R107 82
R108 100
R109 82
R110 82
R111
R112 110
R113 68
R114
R115 39
R116 390
R117 3.6K
R118 220
R119 1.5K
R120 1K
R121 470
R122 470
R123 1K
R124 1K
R125 30
R126 3.6K
R127 1K
R128 5K
R129 3.6K
R130 220K
R131 220K
R132 3.6K
R133 3.6K
R134 1K
R135 1K
R136 1K
R137 1K
R138 470
R139 150
R140 220
R141 220
R142 220
R143 680
R144 150
R145 150
R146 3.9
R147 3.9
R148 220
R149 220
R150 330
R151 330
R152 300
R153 3K
R154 2K
R155 4.3K
L1 6.8µh
L2 0.10µh
L3 6.8µh
L4 6.8µh
L5 6.8µh
L6 1.2µh
L7 .47µh
L8 6.8µh
L9 6.8µh
L10 6.8µh
L11 6.8µh
L12 6.8µh
L13 6.8µh
L14 6.8µh
L15 6.8µh
L16 6.8µh
L19 6.8µh
L22 390µh
TP1
TP2
TP3
TP4
TP5
TP13 BIAS
TP14
TP15
+5
+5V
+5V COIL
-5.2V
-5.2V COIL
-5.2V (2000 BITS)
-5.2V DATA INPUT
-5
GND
CONTROL VOLT
SEE NOTE 6
PHASE
LEVEL
AGC
AGC MONITOR
FROM MC10124
CONTROL A
DS3 INPUT
SIGNAL LOSS
ECL
TTL
EXT NRZ DATA
DATA AC-DC
AC
DC
OVTTL
MC10107
10115
LM733
ZN4403
2N4401
2N4403
IN4148
5082-3080
5082-2800
CA3045
KV1502
MC1648
1747
10231
10131
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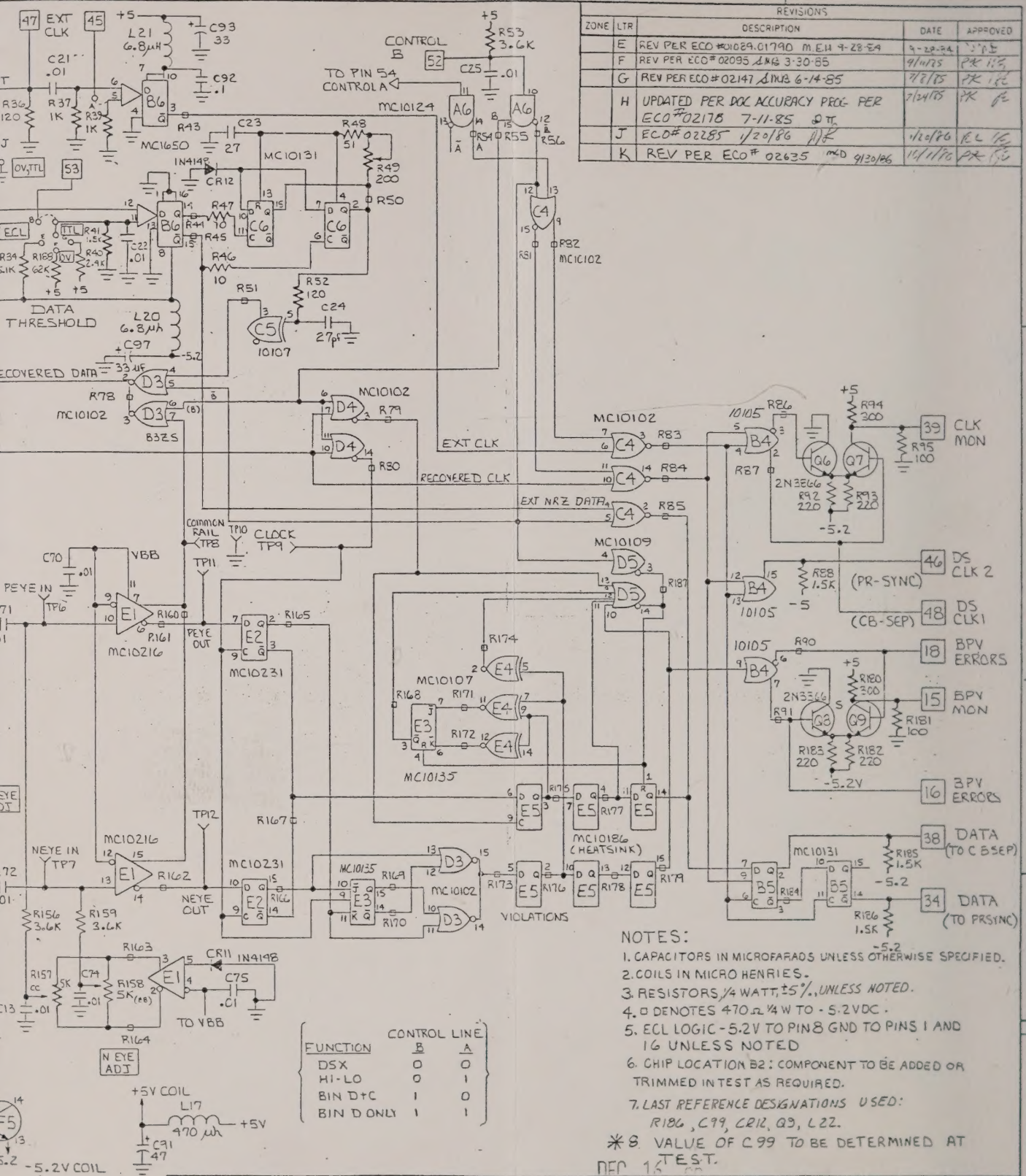

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	E	REV PER ECO #01029-01790 M.E.H 9-28-84	9-28-84	
	F	REV PER ECO# 02095 ΔNB 3-30-85	4/1/85	PK
	G	REV PER ECO # 02147 ΔNB 6-14-85	7/2/85	PK
	H	UPDATED PER DOC ACCURACY PROG PER ECO#02178 7-11-85 DT	7/24/85	PK
	J	ECO# 02285 1/20/86 AJR	1/20/86	RCL
	K	REV PER ECO # 02635 MCD 9/30/86	10/1/86	PK

FUNCTION	CONTROL LINE B	A
DSX	0	0
HI-LO	0	1
BIN D+C	1	0
BIN D ONLY	1	1

NOTES:

1. CAPACITORS IN MICROFARADS UNLESS OTHERWISE SPECIFIED.
2. COILS IN MICRO HENRIES.
3. RESISTORS, 1/4 WATT, ±5%, UNLESS NOTED.
4. □ DENOTES 470Ω 1/4 W TO -5.2VDC.
5. ECL LOGIC -5.2V TO PIN 8 GND TO PINS 1 AND 16 UNLESS NOTED
6. CHIP LOCATION B2: COMPONENT TO BE ADDED OR TRIMMED IN TEST AS REQUIRED.
7. LAST REFERENCE DESIGNATIONS USED: R186, C99, CR12, Q9, L22.

*8. VALUE OF C99 TO BE DETERMINED AT TEST.

DEC 16

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

D-57013	552000
NEXT ASSY	USED ON
APPLICATION	

CONTR NO.

DRWN ROLLINS 12-8-80
CHKD 9-28-84
APPD
APPROVED
BY DIRECTION OF

tau-tron inc

DRAWING TITLE
B3ZS INPUT SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	4	57011	K
SCALE	552000	SHEET 1 OF 1	

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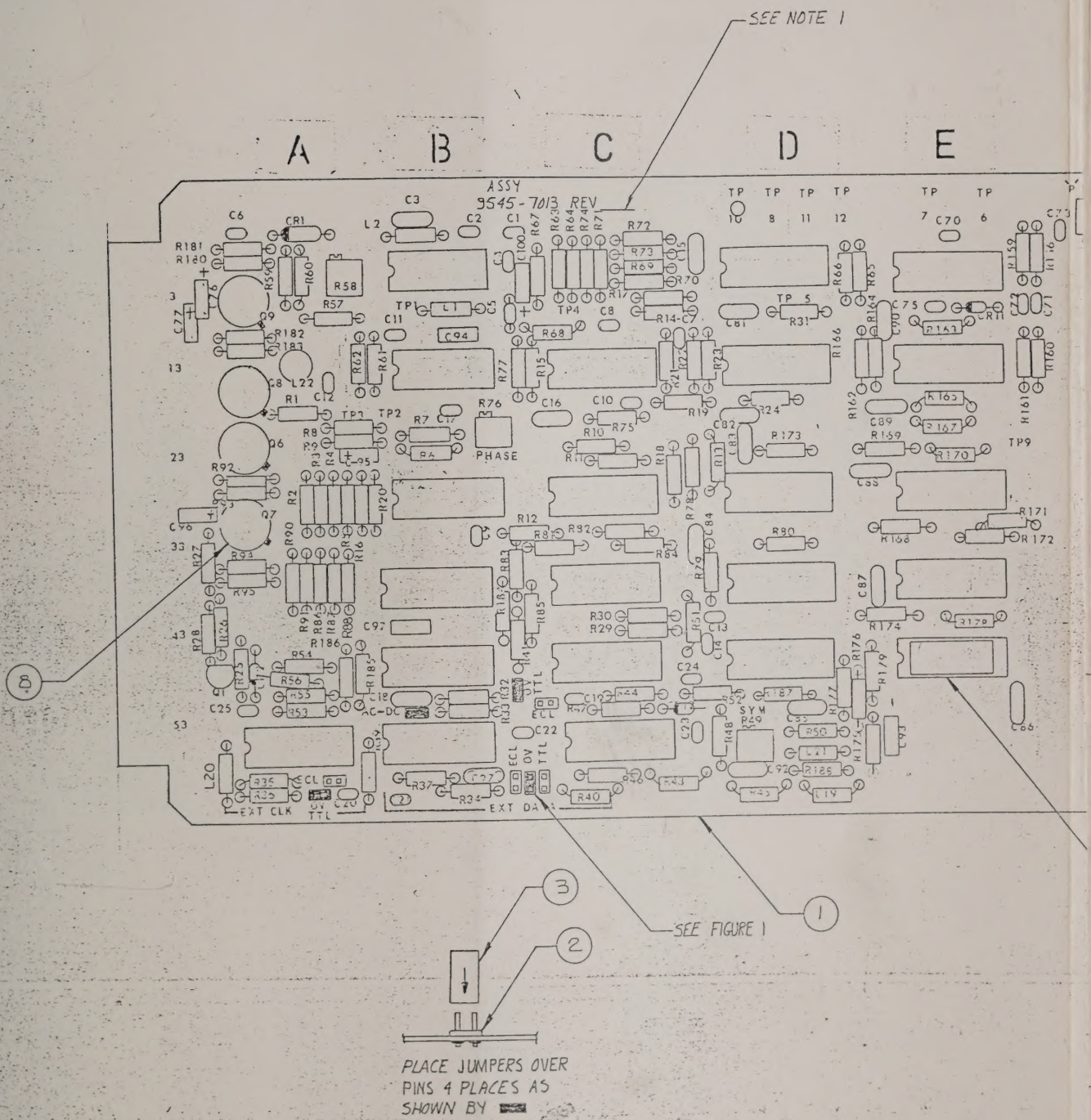
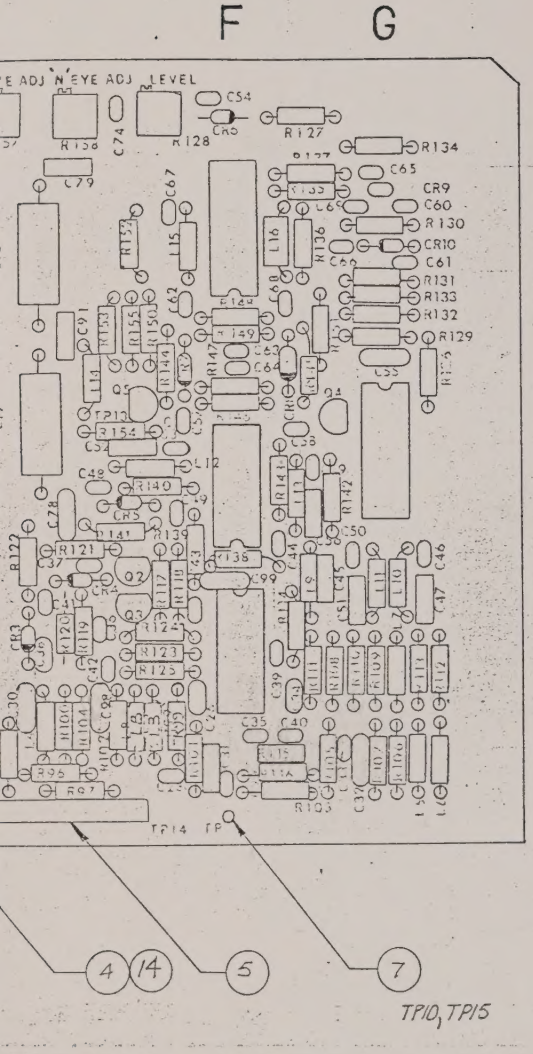


FIGURE 1

	K	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO #02178 7-3-85 DH	7/23/85	PR
	L	REV PER ECO# 02208 TMM 8-15-85	9/12/85	RL
	M	REV PER ECO# 02262 TMM 10/9/85	11/8/85	PR
	N	REV PER ECO# 02263 TMM 11/26/85	1/24/86	RL



NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. INSTALL SCREWS FROM TOP SIDE.

DEC. 16. '86

D-57626	55250
E-57158	55200E RX
D-57350	55200D RX
NEXT ASSY	USED ON
APPLICATION	

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO .1/64

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES ANGLES ± 1°
FRACTIONS ± 1/64
3 PLACE DECIMALS ± .010
2 PLACE DECIMALS ± .01
1 PLACE DECIMALS ± .1
MATERIAL:

CONTR NO
DR MOWRY 11-20-80
CHK B. COOPER 12-8-80
ENGR W. ROLLINS 12-8-80
APPROVED
REVISION LTR

tau-tron

DRAWING TITLE
B3 ZS INPUT PCB ASSEMBLY

SIZE D | CODE IDENT NO 9[illegible] | DRAWING NO 57013 | REV N

SCALE 2/1 | 55200D RX | SHEET 1 OF 2

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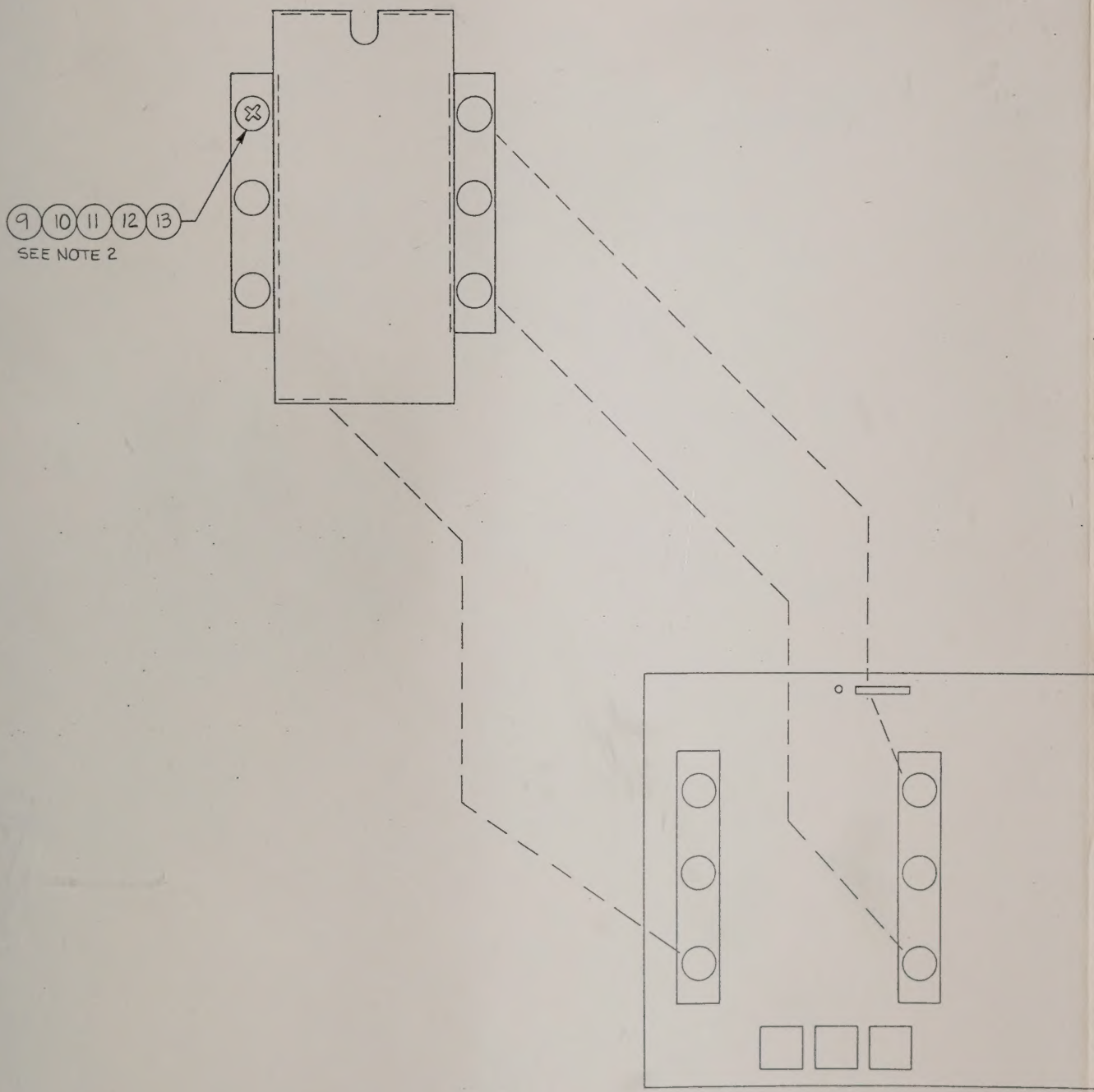
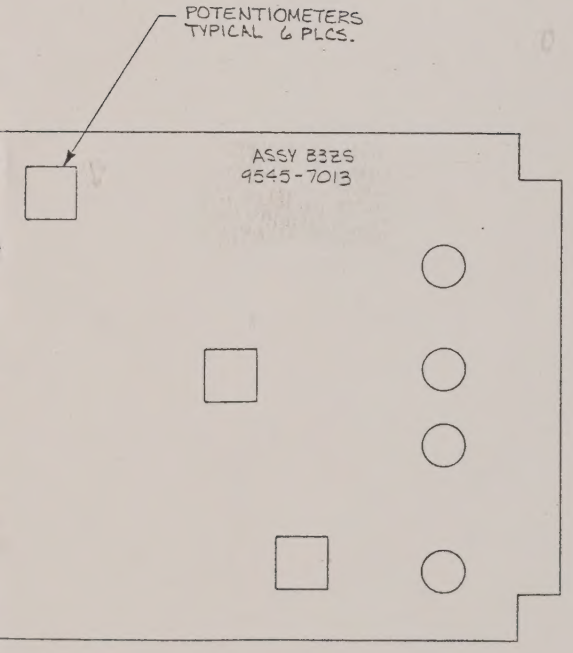


REVISIONS			
ZONE	LTR	DESCRIPTION	DATE	APPROVED
SEE SHEET ONE FOR REVISIONS			



DEO 14 884

D-57626	S5250
E-57158	S5200E RX
D-57350	S5200D RX
NEXT ASSY	USED ON
APPLICATION	

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± 1°
FRACTIONS ± 1/64
3 PLACE DECIMALS ± .010
2 PLACE DECIMALS ± .01
1 PLACE DECIMALS ± .1
MATERIAL:

CONTR NO. U. 10. 00

DR	T. MURPHY	8-15-85
CHK		
APPD		

APPROVED
BY DIRECTION OF

tau-tron
A UNIT OF GENERAL SIGNAL

DRAWING TITLE
B3ZS INPUT
PCB ASSEMBLY

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D		57013	N
SCALE 2/1		5200D RX	SHEET 2 OF 2

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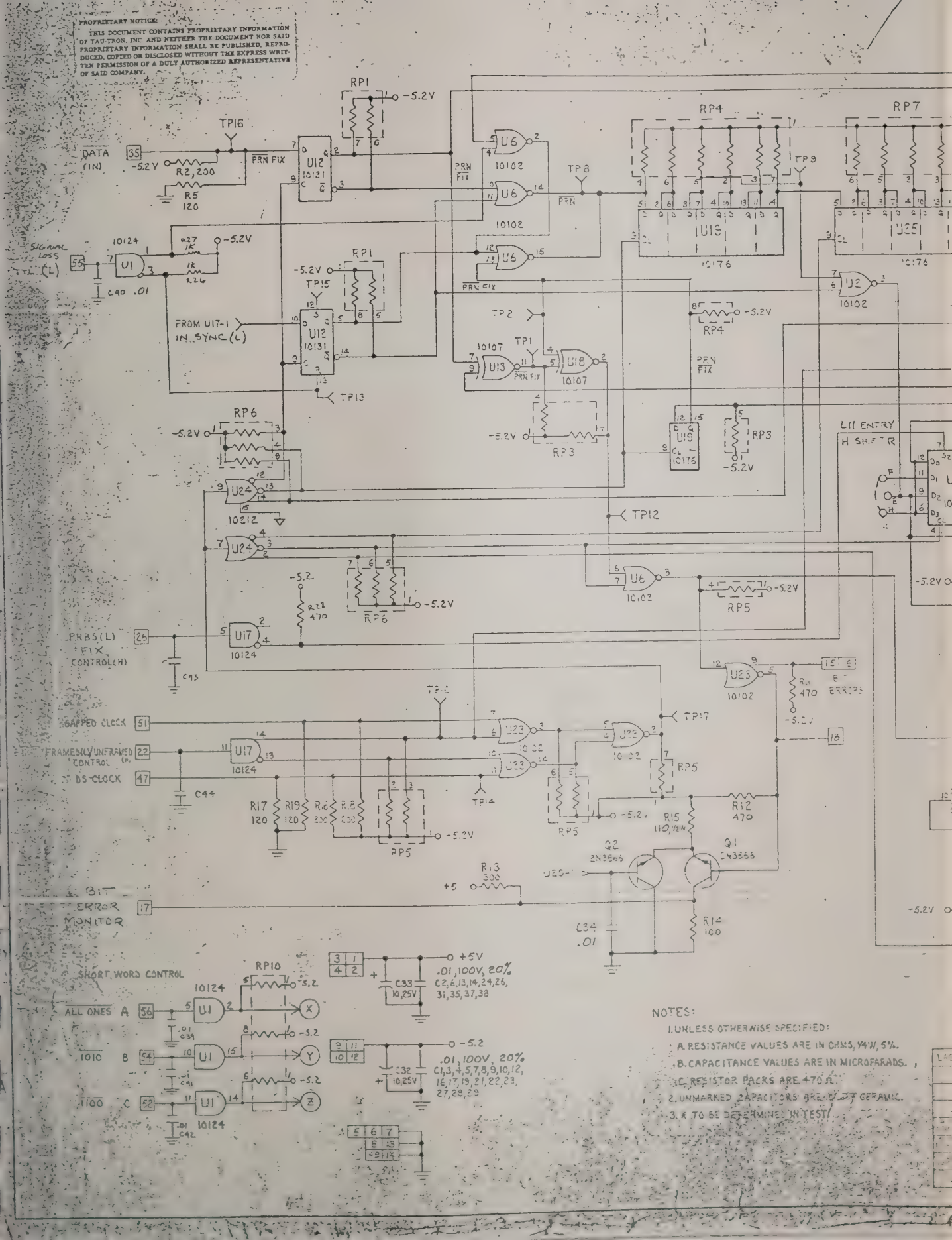


NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   - A. RESISTANCE VALUES ARE IN OHMS, ¼W, 5%.
   - B. CAPACITANCE VALUES ARE IN MICROFARADS.
   - C. RESISTOR PACKS ARE 470 Ω.
2. UNMARKED CAPACITORS ARE .01 μF CERAMIC.
3. * TO BE DETERMINED IN TEST.

ZONE	LTR	DESCRIPTION	DATE	APPROVED
	3	REVISED AND REDRAWN CS 4-10-80		
	4	REV PER PDC, 00795 MRC	10-4-81	EK
	A	REV & RELEASE PER DCN 00793 WM	1-22-81	BAC
	B	REV. PER ECO #1338 25FEB83	2-25-83	PCL
	C	UPDATED PER DOC ACCURACY PROG PER ECO #02178 7-8-85 DH	7/2/85	PK

-5.2V RP8 RP9 R24 470 10107 U8 U14 10176 U4 10178 U3 U9 U4 10102 U20 10125 +5V R23 10K C30 4700pf R25 470 U27 74LS123 PR SYNC U20-1 (Vbb) RP1 TO U13-10

PRN FIX C11, 1000pf C15, 1000pf RP3 R6, 10K R3, 10K TP3 TP5 TP6 U13 U18 U2 10102 RP2 TP4 U12 74LS490 U10 74LS00 R4 470 R7 470 U16 10138 R21, 7.5K C36 .01 R22 10K C16 .01 RP5 U5 74LS74 U21 74LS11 TP11 OUT SYNC OVER THRESHOLD U11 7474 IN SYNC (L=LOS) U26 TP7 U25 U22 74LS123

DATA SYNC
IN SYNC ≥ 40/100
OUT ≥ 20/100 FOR
8 SUCCESSIVE TRIALS

REFERENCE DRAWINGS:
ASSEMBLY: TDK-57016
FABRICATION: CS-57015

PART DES. USED	UNUSED
6	
C	
8	R1
4	
7	

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

TDK-57016	SS2000 RX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

CONTR NO.
DR C. Smith 4-10-80
CHK [illegible] 4-11-80
APPD K. Prince P. 4-14-80
APPROVED:
BY DIRECTION OF

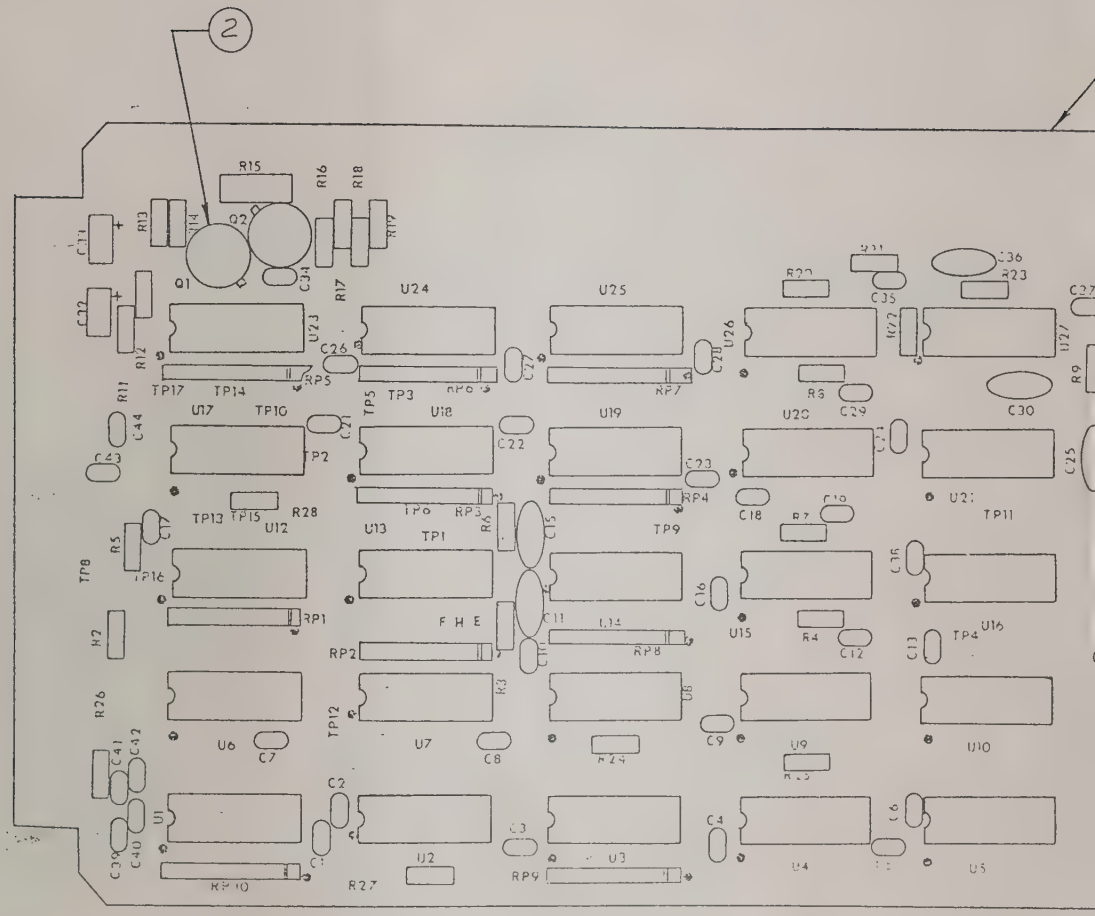
tau-tron INC Chelmsford MA 01824

DRAWING TITLE: SCHEMATIC P RSYNC

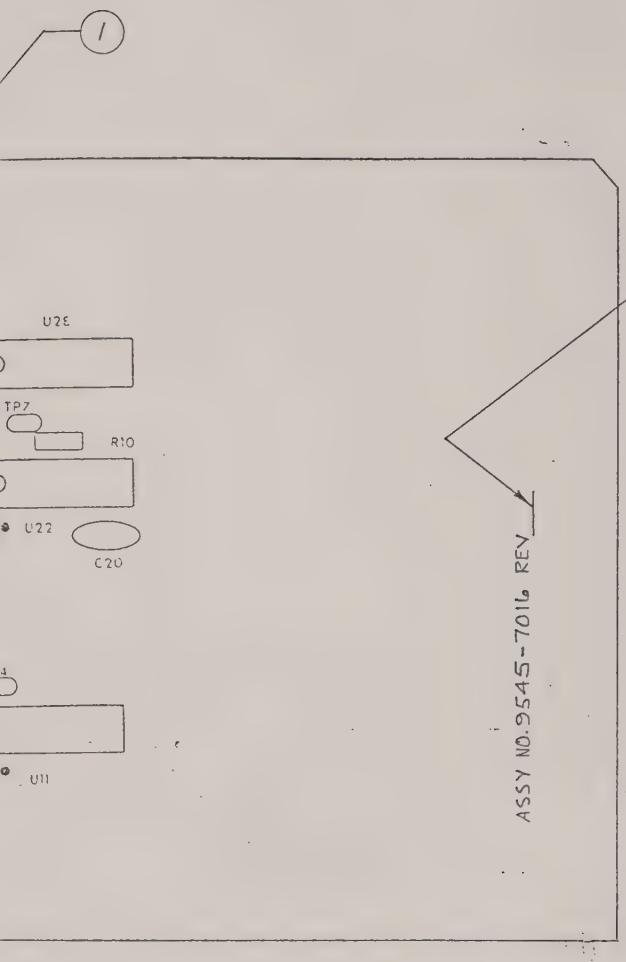
SIZE	CODE IDENT NO	DRAWING NO.	REV
D		57014	C

SCALE: NONE SHEET 1

CONFIDENTIAL INFORMATION



	D	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO#02178 7-1-85 DH	7/31/85



NOTES:

1. PLACE A PERMANANT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

DES. C.A.
DWN B.C.
APD K.B.

tau-tron

ASSEMBLY
P.R. SYNC P.C. BD.

TER-57158 552000 RX
NEXT ASSY | USED ON
APPLICATION

D | 954 | 57016 | D

SCALE 2/1 | 552000 RX | SHEET 1 OF 1

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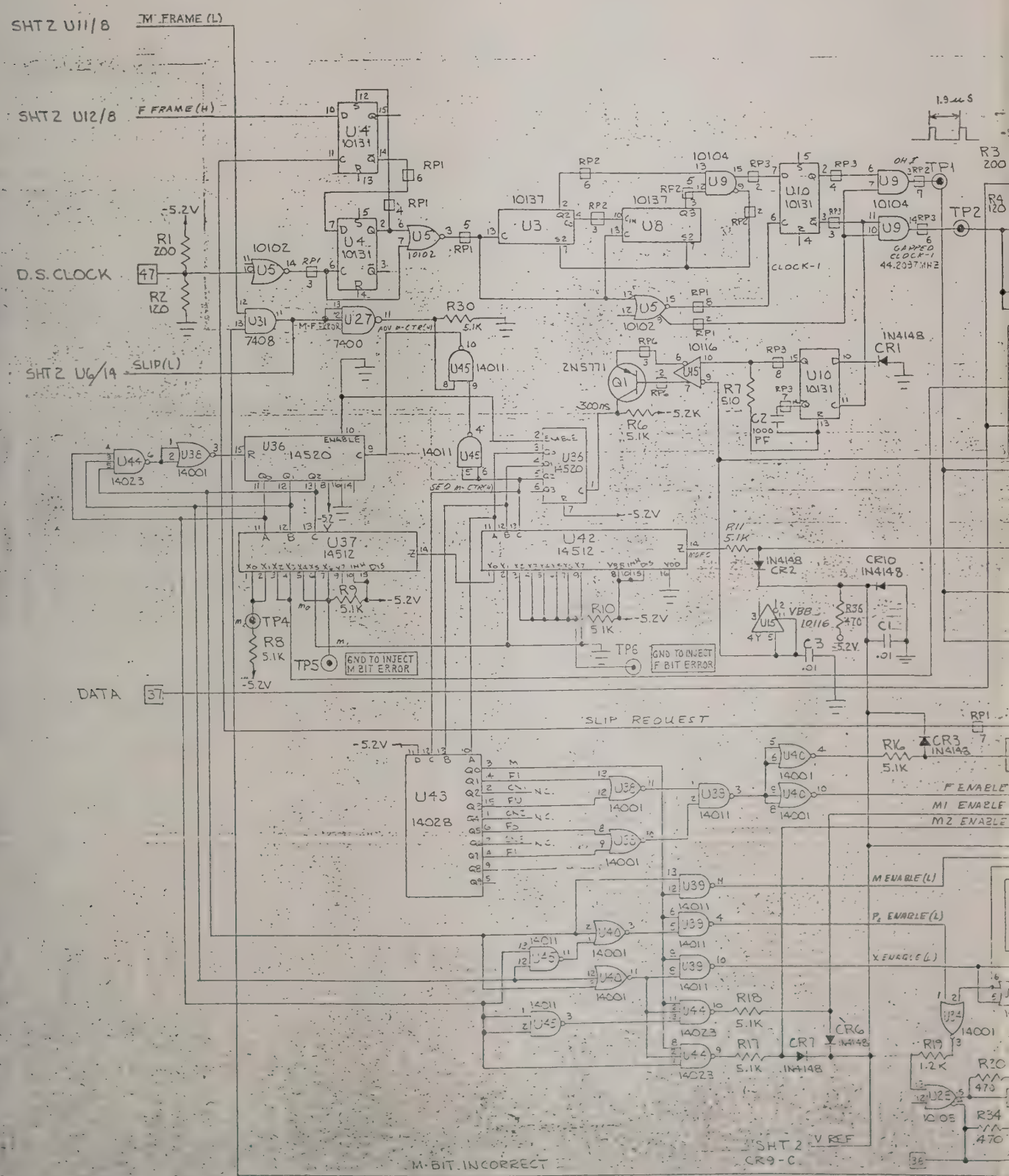

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	A	REL. PER ECO #01968 9-14-84	9-17-84	
	B	UPDATED PER DOC ACCURACY PROG PER ECO #02178 7-9-85 DM	7/24/85	

NOTES:
1. UNLESS OTHERWISE SPECIFIED:
   A. RESISTANCE VALUES ARE IN OHMS, ±5%, 1/8 W.
   B. CAPACITANCE VALUES IN MICROFARADS.
2. ⊖ DENOTES 470 OHM RESISTOR (SIP PKG.) TO -5.2V
3. ☐ DENOTES EDGE CONNECTOR PIN NO. OR LETTER.
4. SEE SHEET 2 FOR LAST USED REFERENCE DESIGNATIONS.

EVEN=1
ODD=0

-5.2V
TP9
TP3
R32 5.1K
ZN5771 Q4
U14 10131
U13 10131
U24 10131
U19 10102
RP5
RP6
ONES COUNT(H)
U15 10116
R5 1.5K
C33 .01
PARITY
48 GAPPED CLOCK
39 LOSS OF SIGNAL (L)
U18 10107
RP4
1=CORRECT
1=INCORRECT
R38 200
R39 330
C34 .01
U25 10116
R40 470
30 FRAME ERROR
32 FRAME ERROR
R41 470
U16 10107
U1 10102
R37 470
U20 10131
R12 510
C4 .01
Q2 ZN5771
R13 5.1K
Q3 ZN5771
R15 5.1K
R31 470
C5 .01
C6 .01
R14 510
SH2 U28-7
M SYNC
U21 7402
CR4 IN4148
R21 5.1K
U26 10105
R22 470
U35 14001
11 MSBIT CORRECT
SHT 2 U 2/3
SHT 2 U 2/4
F-BIT CORRECT
SHT 2 U27/4
F-BIT INCORRECT
SHT 2 U32/6 & 8
U29 10125
24 X-BIT
U34 14001
U33 10131
PARITY ERROR
PARITY ERROR SH 2 U29-3

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±	CONTR NO.
	MATERIAL:	DR D. OLIVER 26DEC79 CHK APPD APPROVED BY DIRECTION OF
NEXT ASSY 5250 / USED ON APPLICATION		

tau-tron inc Chelmsford, Ma. 01824

DRAWING TITLE
CONTROL BIT SEPARATOR SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	X	57687	E

SCALE NL | 5250 | SHEET 1 OF 2

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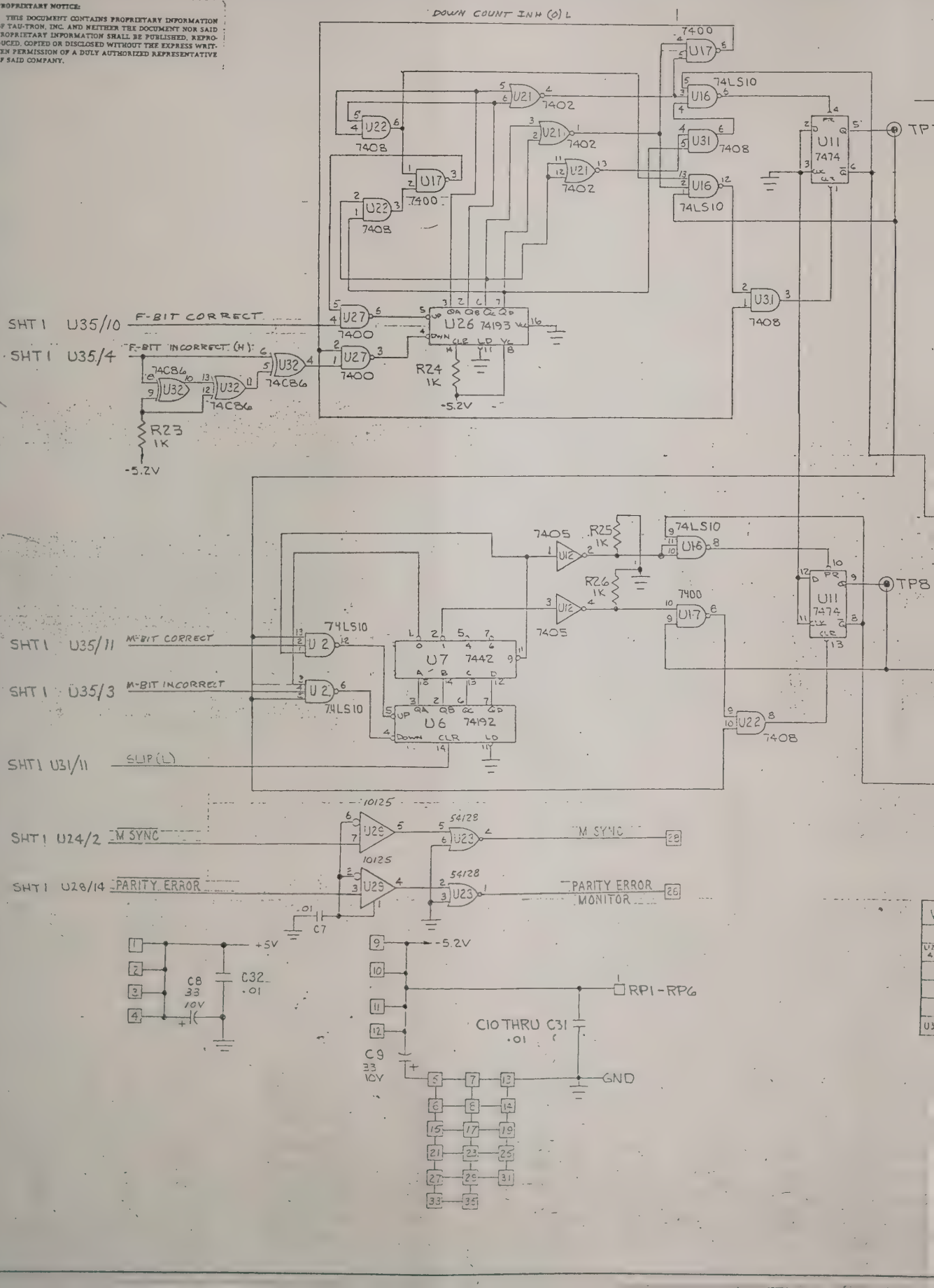
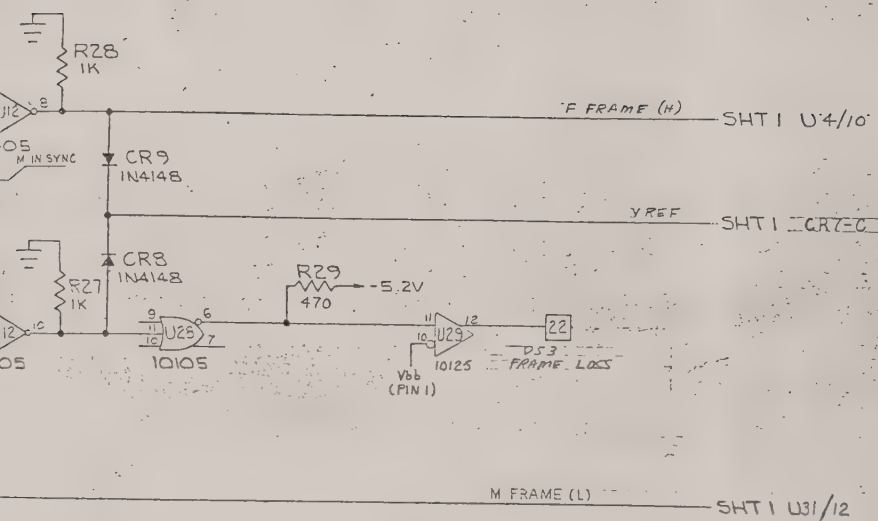


REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
		SEE SHEET 1 FOR REVISIONS		

F IN SYNC



TAGE, GROUND CONNECTION TABLE FOR IC'S			
REF DES	GND PIN	-5.2V PIN	+5V PIN
4,45	14	7	——
U5,7,26,36,37,42,43	16	8	——
U23	7	——	14
U29	16	8	9
8,9,10,13,14,15,18,19,20,24,25,28	1,16	8	——

LAST REF DES USED	UNUSED
U45	U1,30,41
C31	
R41	
Q4	
CR10	CR5
TP9	
RP6	

DEP 13

	5250
NEXT ASSY	USED ON
APPLICATION	

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± ~
FRACTIONS ± ~
3 PLACE DECIMALS ± ~
2 PLACE DECIMALS ± ~
1 PLACE DECIMALS ± ~
MATERIAL: ~

CONTR NO.

DR	D. OLIVER	26DEC79
CHK	K.A. Fraser	26 Dec 79
APPD	W. Ertl	27 Dec 79
APPD	[illegible]	2/1/80
APPD	[illegible]	2/19/80

APPROVED /

BY DIRECTION OF SH Mulholland

tau-tron INC Chelmsford MA 01824

DRAWING TITLE
CONTROL BIT SEPARATOR SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	~	57687	3
SCALE ~	5250	SHEET 2 OF 2	

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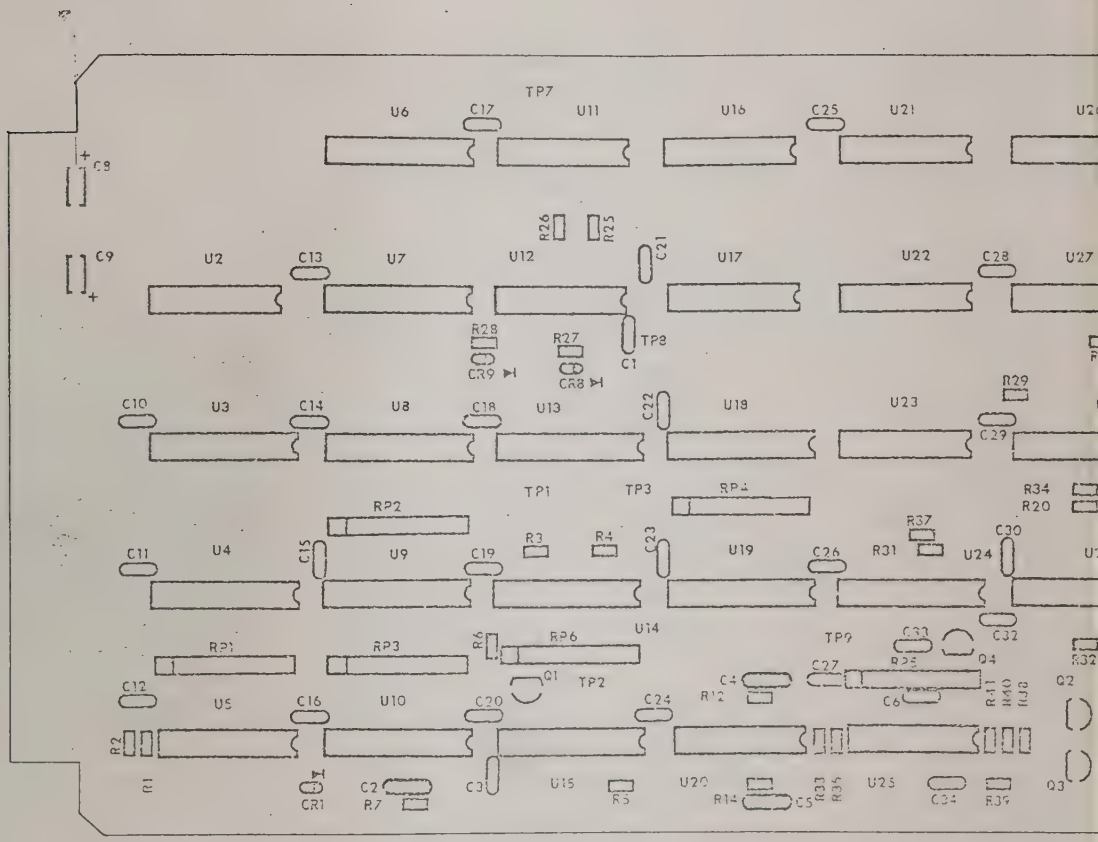
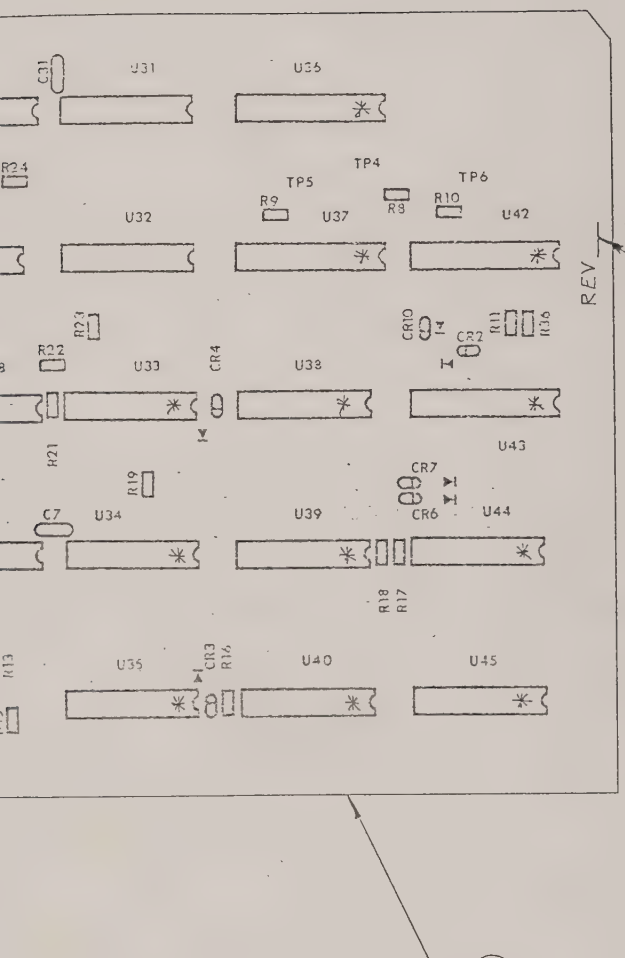


	B	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO #02178 7-1-85 DH	7/24/85	



SEE NOTE 2

NOTES:

1. * DENOTES A STATIC SENSITIVE DEVICE.
2. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

DEC. 16, '86

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO .164	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES ± 1° FRACTIONS ± 1/64 2 PLACE DECIMALS ± .01 3 PLACE DECIMALS ± .005 4 PLACE DECIMALS ± .0005 MATERIAL	DRAWN BMB 5-22-84 CHECKED JE 6-1-84 ENGR AC 9/19/84 APPROVED 9/20/84	tau-tron GENERAL SIGNAL
57690 / 5250 NEXT ASSY / USED ON		APPROVED	DRAWING TITLE ASSEMBLY CONTROL BIT SEPARATOR
APPLICATION		RELEASED SAM 9/7/89	SIZE D / CODE IDENT NO. 954 / DRAWING NO. 57689 / REV B
			SCALE 2/1 / 5250 / SHEET 1 OF 1

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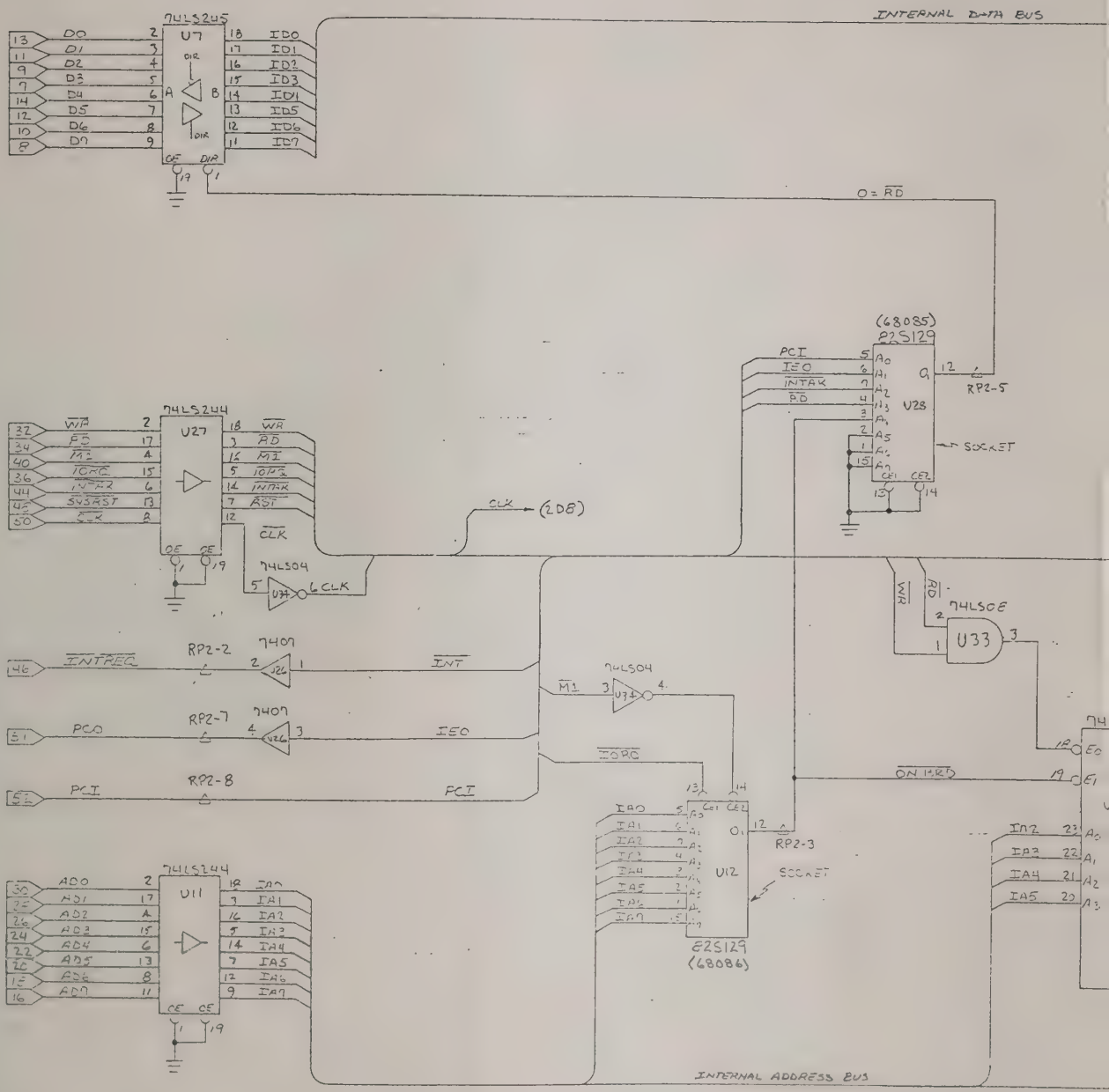
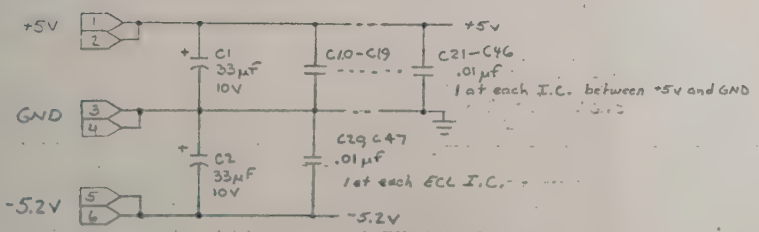
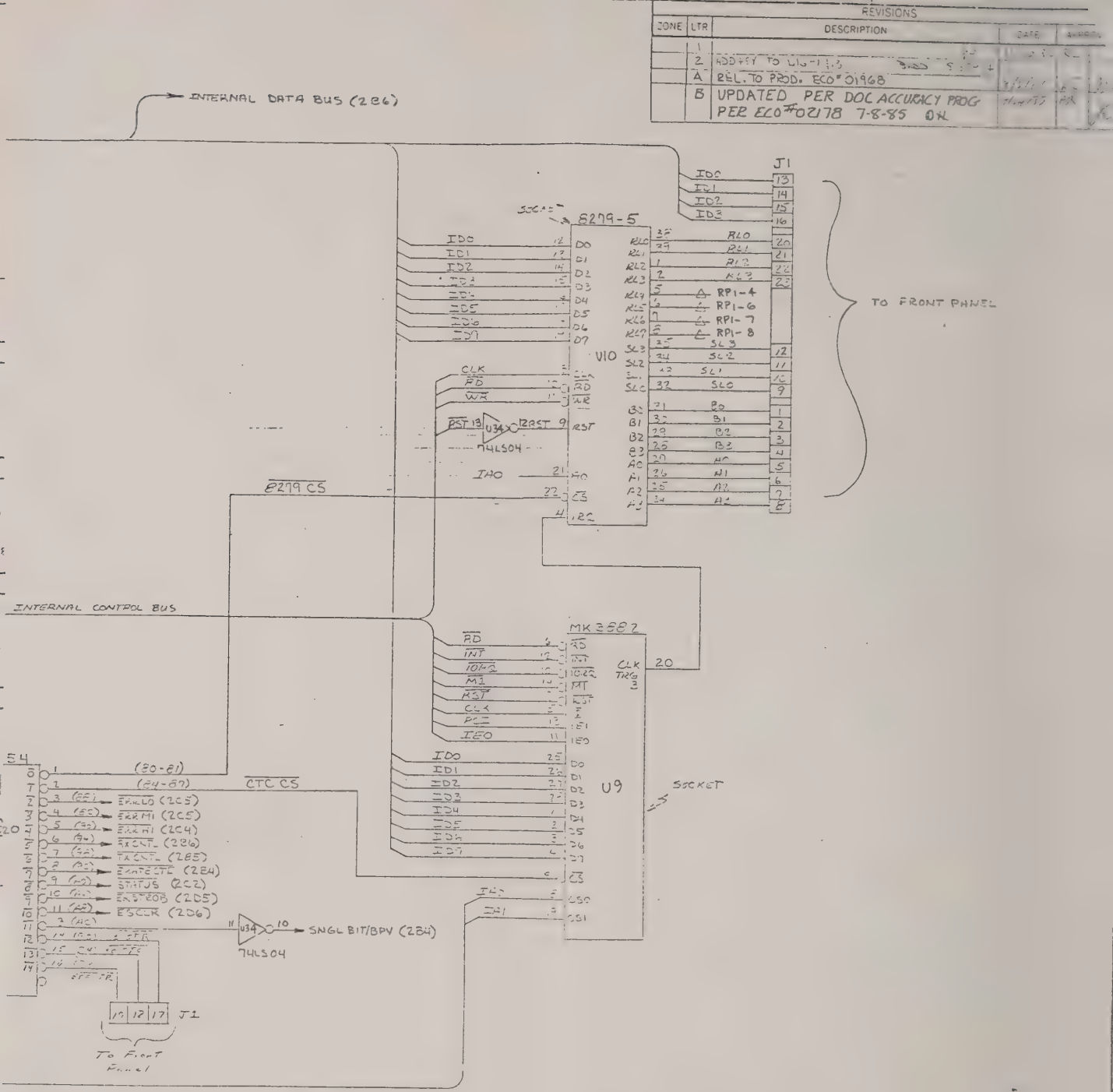




NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   A. RESISTANCE VALUES ARE IN OHMS 1/8
   B. CAPACITANCE VALUES ARE IN MICROFARADS
2. ▭ = Card edge connector.
3. ⏚ = 4.7KΩ 1/8w (SIP) to +5v.

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	1			
	2	ADDED Y TO U6-11,13		
	A	REL. TO PROD. ECO#01968		
	B	UPDATED PER DOC ACCURACY PROG PER ECO#02178 7-8-85 DH		



1/+5%.

REFERENCE DESIGNATIONS	
ST USED	UNUSED
29	
3	
15	
32	
33	
13	

WHERE APPLICABLE:
REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

57634	S5250
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± 4
FRACTIONS ± 1/4
3 PLACE DECIMALS ± .001
2 PLACE DECIMALS ± .01
1 PLACE DECIMALS ± .1
MATERIAL: 4

ECO 16 [illegible]
CONTR NO

DR	[illegible]	7-8-82
CHK	[illegible]	7/11/82
APPD	[illegible]	7-1-82
	[illegible]	9-9-82
	[illegible]	

APPROVED
BY DIRECTION OF [illegible] 7/11/84

tau-tron INC Chelmsford, MA 01824

DRAWING TITLE
S5250
PERIPHERAL CNTL/COUNTER

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	4	57634	B

SCALE [illegible] | S5250 | SHEET 1 OF 1

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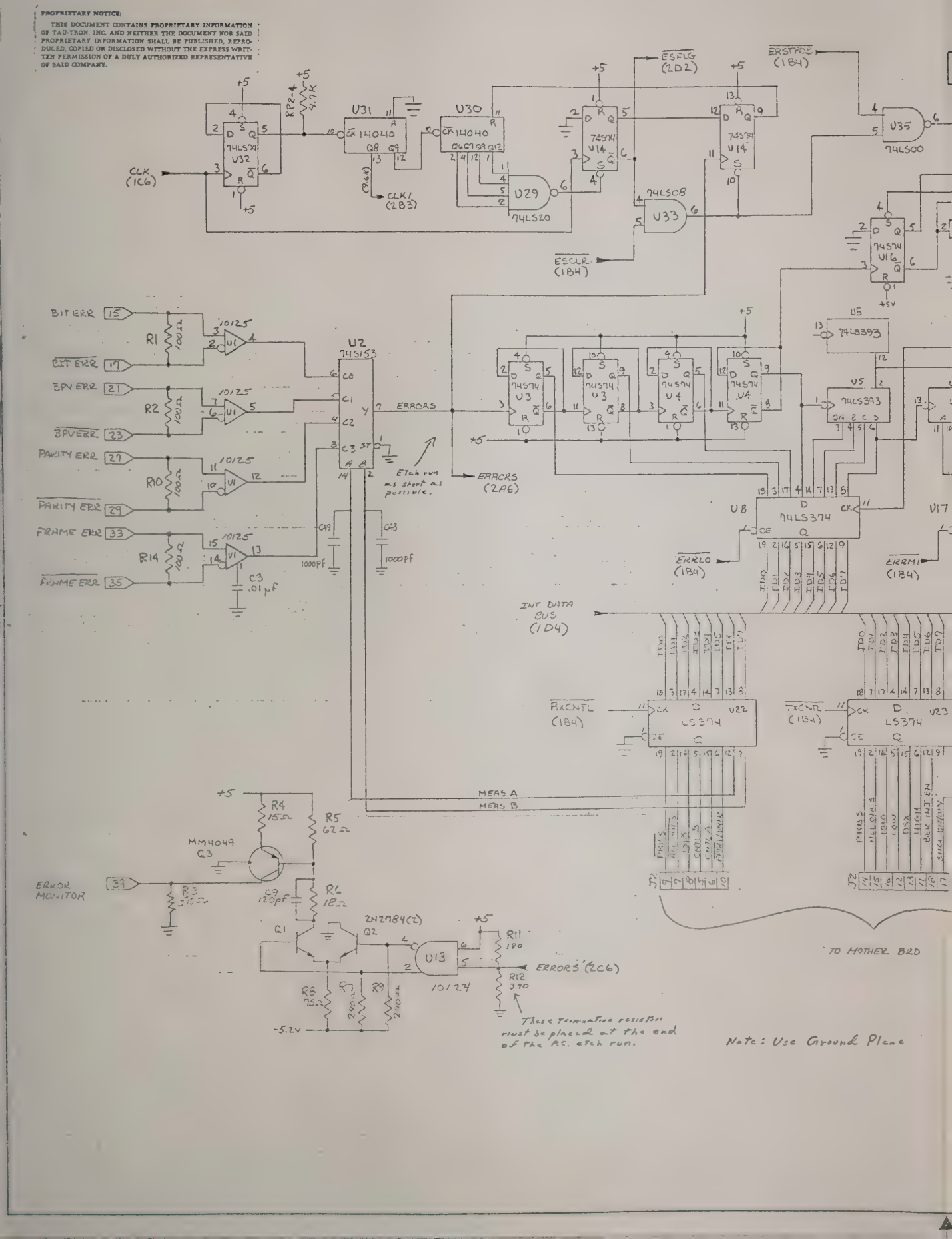
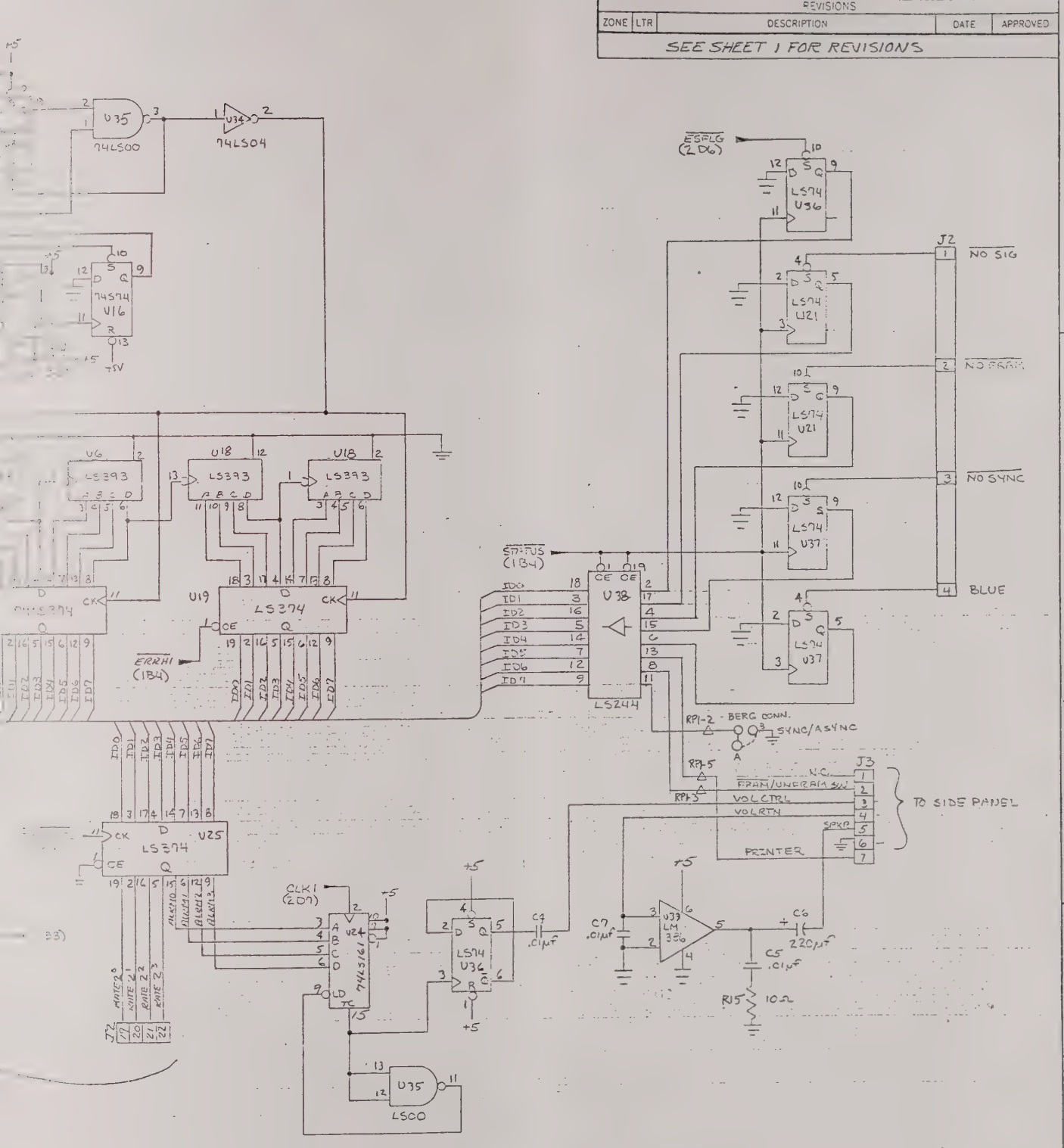


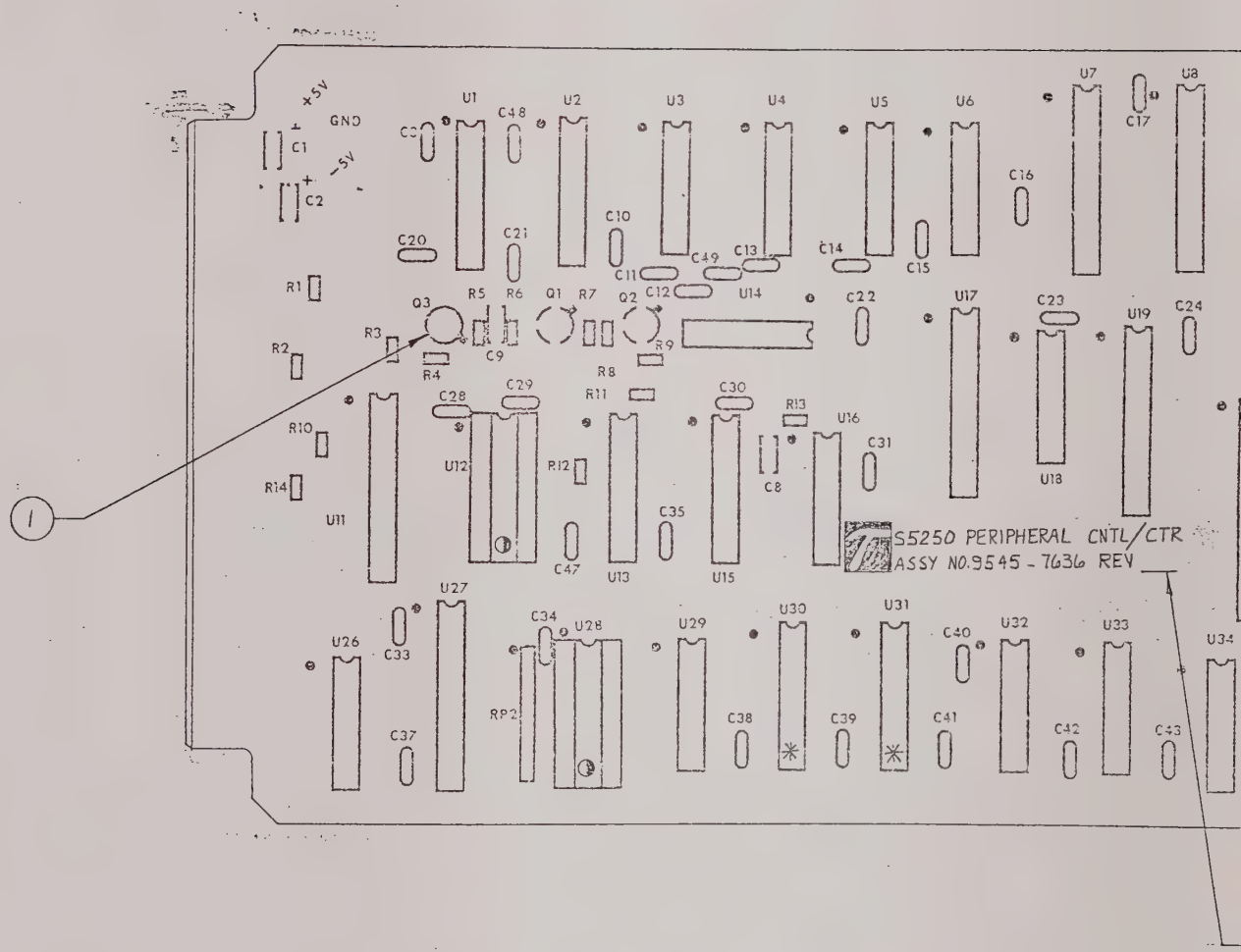
REVISIONS			
ZONE LTR	DESCRIPTION	DATE	APPROVED
SEE SHEET 1 FOR REVISIONS			



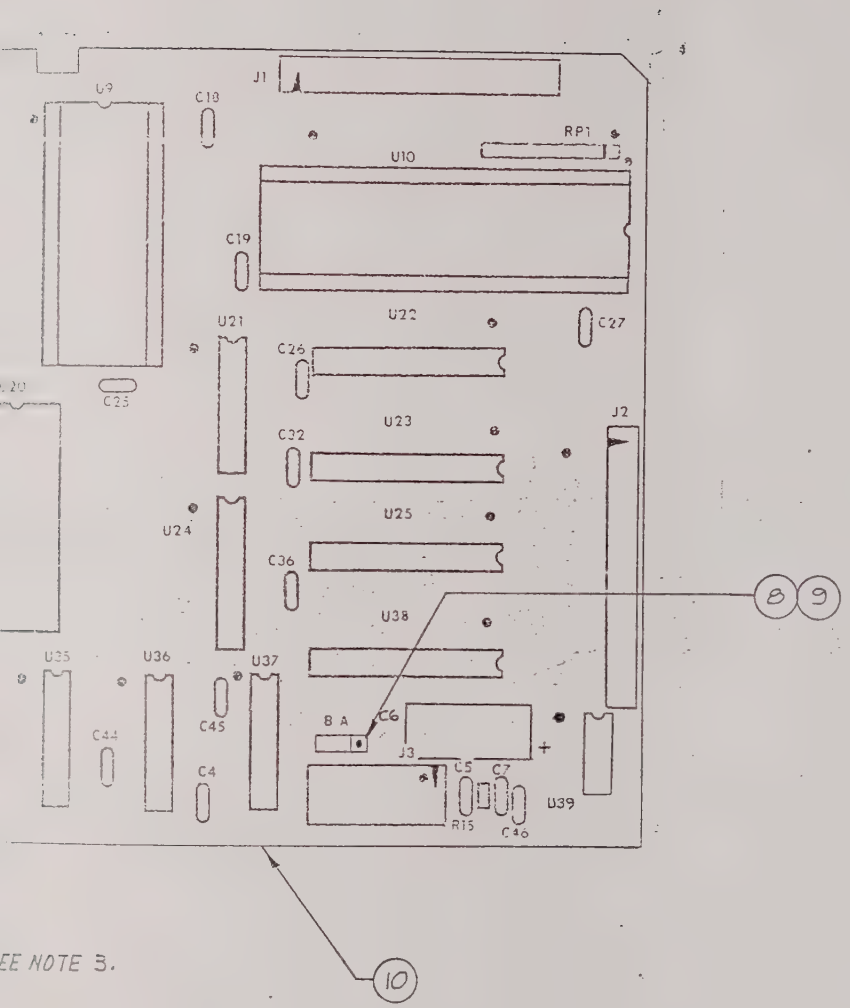
WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±	CONTR NO. DLO. 10.	DR [illegible] 7-8-83 CHK [illegible] 9-1-83 APPD [illegible] 9-1-83 [illegible] 9-19-83 [illegible] 9-17-83	tau-tron inc Chelmsford, ma. 01824	
	MATERIAL:	APPROVED	BY DIRECTION OF SM Mulcahy 9/17/83	DRAWING TITLE 55250 PERIPHERAL CNTL/COUNTER	
D-57636 / 55250				SIZE D / CODE IDENT NO. / DRAWING NO. 57634 / REV B	
NEXT ASSY / USED ON				SCALE — / 55250 / SHEET 2 OF 2	
APPLICATION					

57634

CONFIDENTIAL INFORMATION



	B	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO # 02178 7-1-85 DH	7/23/85	RK
	C	REV/ECO # 02390 5/7/86 RJA	5/12/86	RL



EE NOTE 3.

NOTES:

1. ✳ DENOTES STATIC SENSITIVE DEVICE.
2. ◑ DENOTES TO BE ASSEMBLED IN TEST.
3. PLACE THE ASSEMBLY PARTS LIST REVISION MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

DEC. 16.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±	CONTR NO	tau-tron GENERAL SIGNAL
		DR B.M.B 9-7-83	DRAWING TITLE ASSEMBLY PERIPHERAL CNTL/CTR S5250
		CHK H.L.L 9-8-83	
		APPROVED AS 9-8-83	
		JE 9.19.84	
		QC 9.19.84	
S52050	MATERIAL	APPROVED	USE ON NEXT ASSY / USED ON / DRAWING NO D / 57636 / REV C
NEXT ASSY / USED ON		BY DIRECTION OF SHM 9.9.86	SCALE 2/1 S5250 SHEET 1 OF 1
APPLICATION			

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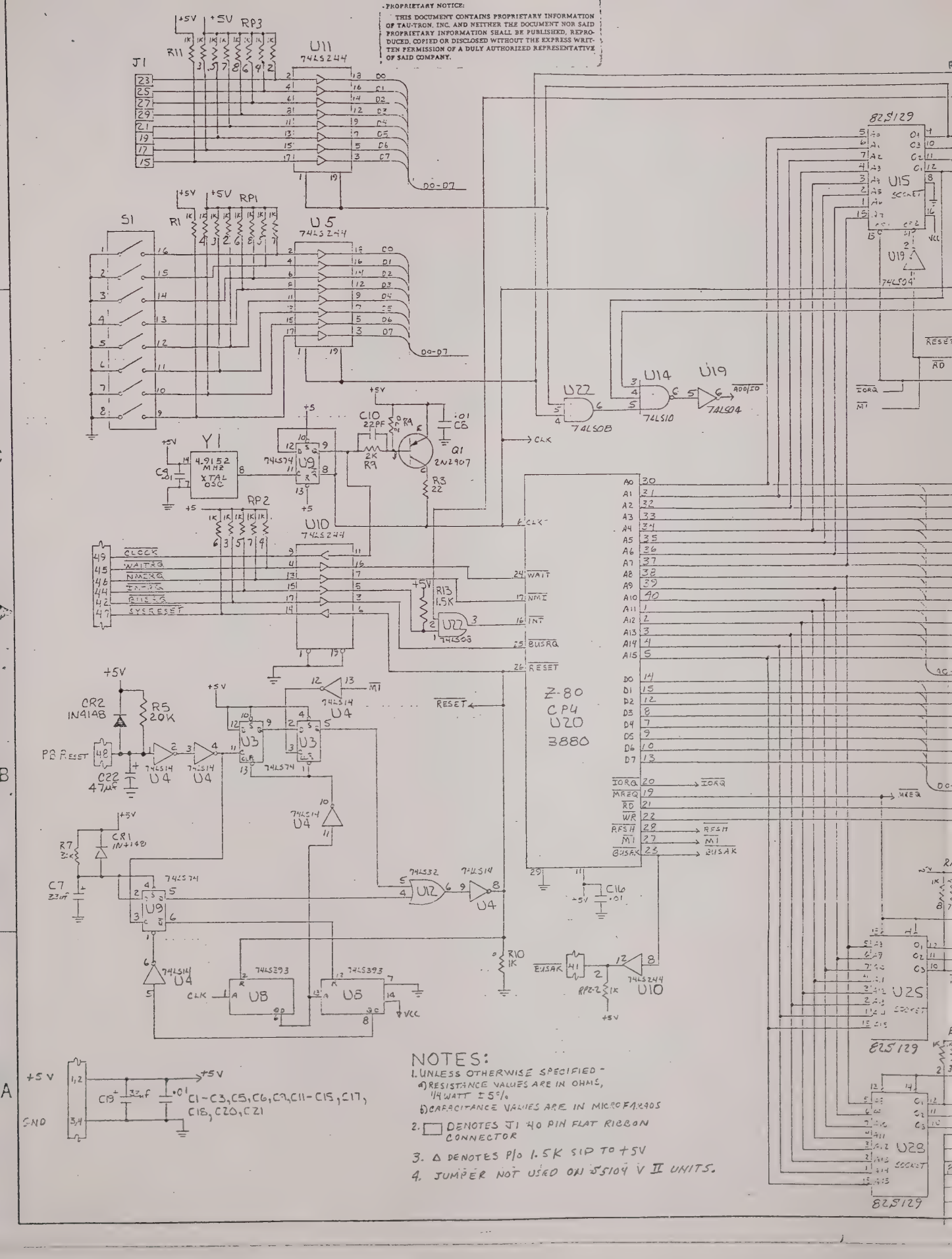

NOTES:

1. UNLESS OTHERWISE SPECIFIED -
   a) RESISTANCE VALUES ARE IN OHMS, 1/4 WATT ±5%
   b) CAPACITANCE VALUES ARE IN MICROFARADS
2. ▭ DENOTES J1 40 PIN FLAT RIBBON CONNECTOR
3. Δ DENOTES P/O 1.5K SIP TO +5V
4. JUMPER NOT USED ON 5510-4 V II UNITS.

ZONE	LTR	DESCRIPTION	DATE	APPROVED
		UNRELEASED	5/25/83	AL
	A	RELEASED PER ECO# 01713 SM 1-23-84	1-24-84	
	B	REV PER ECO# 01742 M.E.H 3-16-84	3-28-84	
	C	REVISED PER ECO# 01805	5-1-84	JE
	D	REVISED PER ECO# 02034	1/16/85	
	E	REVISED PER ECO# 02180	7/22/85	
	F	REV PER ECO# 02639	11/17/86	

U1 74LS125

U22 74LS08

U1 74LS125

PC1-IN

PC0

LAST EI0

J K M N P

Z-80 CTC U27

Z-80 DART U18

3882

D0-D7

74LS 244 U17

74LS 244 U24

J1

+5V

74LS 244 U23

74LS 373 U26

74LS 244 U29

74LS 244 U16

74LS32 U12

74LS04 U19

U21 2KX8

SOCKET

U13 A57712

U7 2764

U2 2764

SEE NOTE #4

MEMSEL1

MEMSEL2

MEMSEL3

MREQ

RD

WR

M1

IORQ

BUSAK

RESET

INT

WAIT

IEI

IEO

CE

CS1

CS0

CLK

A0 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12

D0 D1 D2 D3 D4 D5 D6 D7

74LS04 U6

74LS32 U12

74LS08 U22

74LS04 U6

74LS10 U14

74LS10 U19

ADD/IO

ME-03

LAST-EI0

RE-IN

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

TOLERANCES: ANGLES ± ~

FRACTIONS ± ~

3 PLACE DECIMALS ± .~

2 PLACE DECIMALS ± .~

1 PLACE DECIMALS ± .~

MATERIAL: ~

CONTR NO.

DR S. [illegible] 11-22-82

CHK RL 11-22-82

APPD K. Rennie 11/28/82

APPROVED

BY DIRECTION OF

tau-tron inc Chelmsford, Ma. 01824

DRAWING TITLE

CPU/RS232 PCB SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	~	57429	F

SCALE 5250 SHEET 1 OF 1

D-57431 | S5250

NEXT ASSY | USED ON

APPLICATION

REFERENCE DESIGNATIONS

IT USED	UNUSED
C22	
U29	

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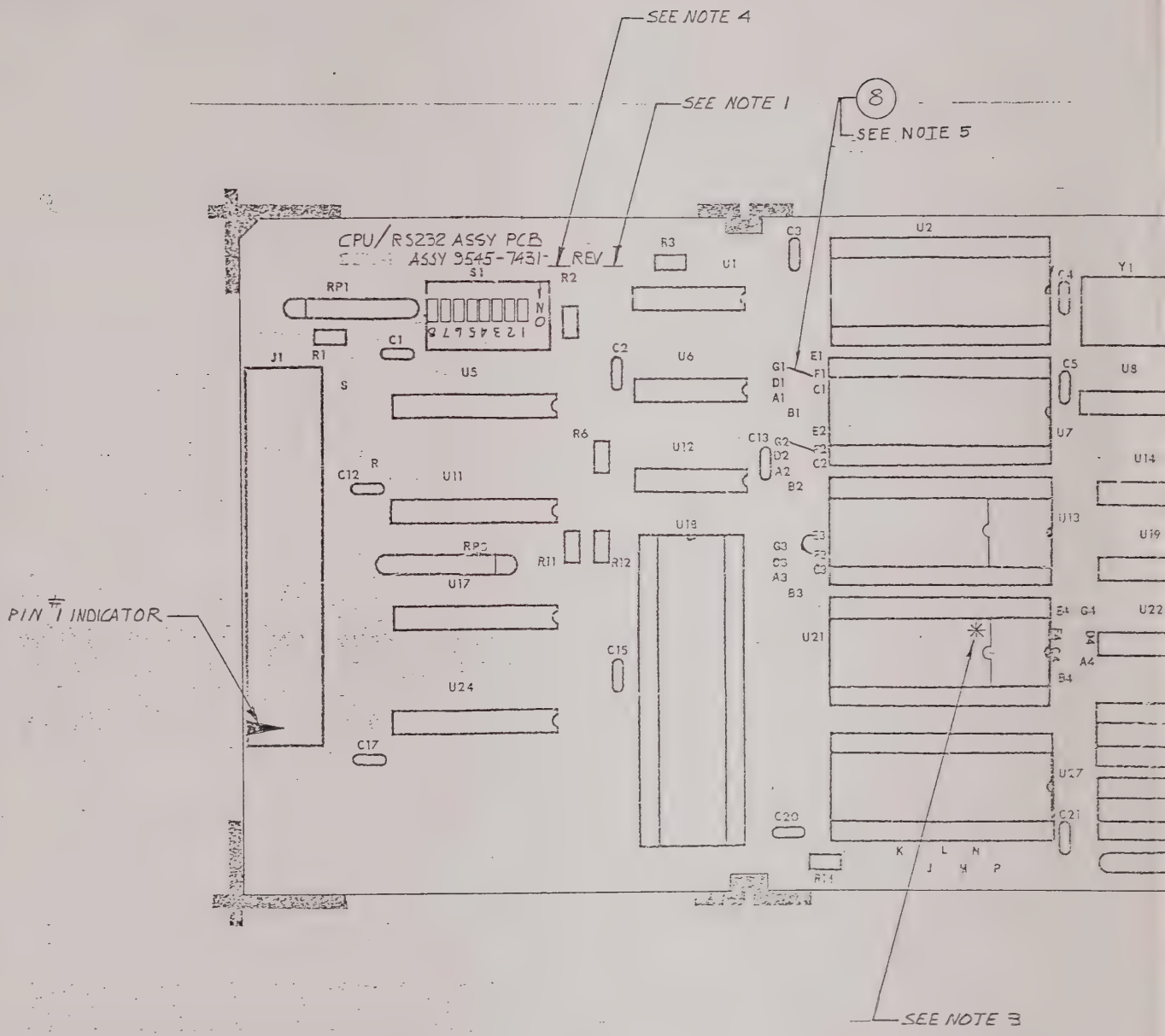
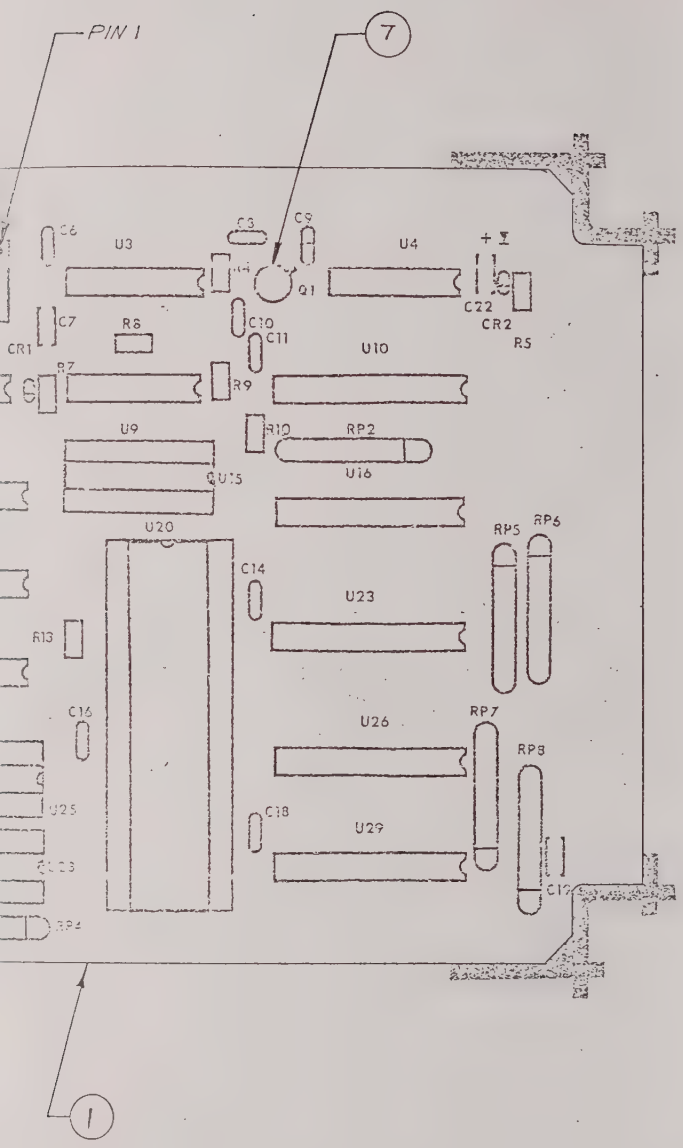


	G	REDRAWN AND REVISED. PER ECO #2180. 7-10-85 BH	9/11/85	RL
	H	REV PER ECO # 02528 MCD 7/9/86	7/10/86	SK



NOTES:

1. PLACE THE ASSEMBLY PARTS LIST REVISION ON THE ASSEMBLY IN THE DESIGNATED AREA.

3. PIN 1 OF I.C. TO BE LOCATED IN PIN 3 OF SOCKET.
4. LEAVE THIS SPACE BLANK UNLESS PARTS LIST IS 9545-7431-1, THEN PERMANENTLY MARK -1.
5. CONFIGURATION JUMPERS F1-G1, F2-G2, E3-F3 TO BE USED ON S5250 ONLY.

DEO 86-206

ANSI APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES ± 1° FRACTIONS ± 1/64 3 PLACE DECIMALS ± .010 2 PLACE DECIMALS ± .01 1 PLACE DECIMALS ± .1	ORDER NO DEO. 10. 86	tau-tron A DIVISION OF GENERAL REGIONAL
		DR B.M.B 11-29-82	DRAWN TITLE ASSEMBLY CPU/RS232 PCB S5104
		CHK H.D.L 11-29-82	
		APP K.B. 11-29-82	
D-57626 S5250	MATERIAL SEE P/L 9545-7431	APPROVED	SIZE D CUSTOMER IDENT NO DRAWING NO 57431 REV H
E-57469 S5104VII		BY DIRECTION OF	SCALE 2/1 S5250 SHEET 1 OF 1
NEXT ASSY USED ON			
APPLICATION			

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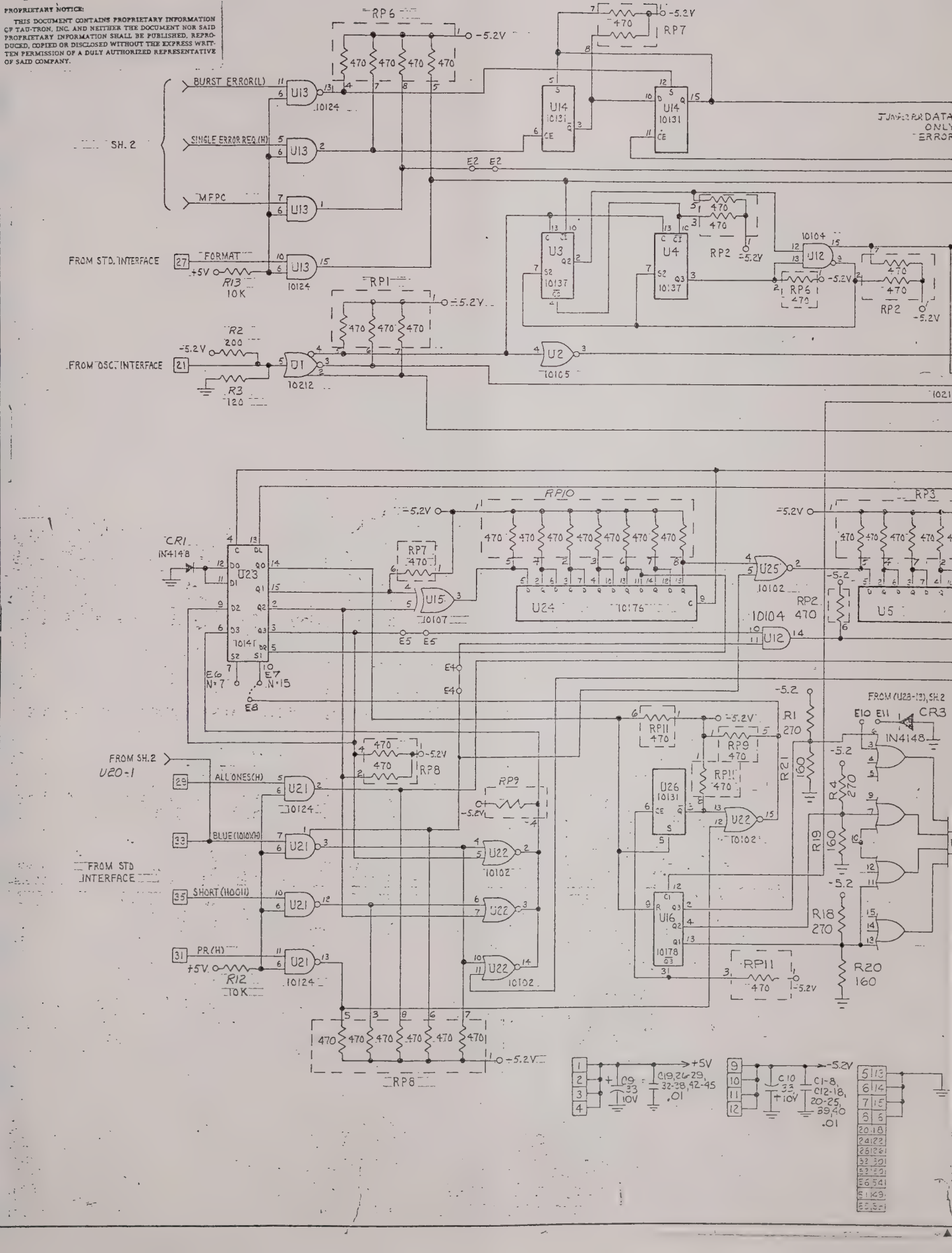

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	4	ORIGINATED 3/29/80 C.G.S.	4-7-80	JEW WJ
	5	REVISED PER PDC 00723 § 1 9-22-80	9-24-80	[illegible]
	A	REVISED & RELEASED PER DCN00770 § 2 12-17-80	1-9-81	[illegible]
	B	REV. PER ECO/00973 4/1 8 SEP 81	9-15-81	[illegible]
	C	REV PER ECO/1408 J RAF 2 MAY 83	6-6-83	[illegible]
	D	REV PER ECO# 1648 M.E.H 12-12-83	12-29-83	[illegible]
	E	REV PER ECO# 02033 YM 2+ 85 JAN 17	1/21/85	[illegible]
	F	UPDATED PER ECO #02181 T.B. 7-19-85	9/19/85	[illegible]

ERROR CONTROL

OVERHEAD BITS

TP-15 DATA [23] TO OUTPUT AMP.

DATA [25] TO OSC. INTERFACE

TP-16 CLOCK-2 [17] TO OUTPUT AMP.

CLOCK-2 [19] TO OSC. INTERFACE

CLOCK → TO SH.2

U16 PARITY → (U18-3), SH.2

U10 OH STROBE → (U27-1), SH.2

U10 PR SYNC [37] TO OCS INTERFACE

TP-14

C11 .01

NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   A. RESISTANCE VALUES ARE IN OHMS, 1/4W, ±5%.
   B. CAPACITANCE VALUES ARE IN MICROFARADS.
2. ⊣ DENOTES 470Ω RESISTOR TO -5.2V
3. ○—○ E5. DENOTES WIRE JUMPER TYP. E1, E2, E4, &

LAST REF DES USED		UNUSED
U43	TP16	
RP11	E17	
R35	Q2	
C45		
CR3		

REFERENCE DRAWINGS:
ASSEMBLY: TDK-57010
FABRICATION: TDG-57009

DEC. 16. [illegible]

TDK-57010	552006 TX
NEXT ASSY	USED ON
APPLICATION	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

CONTR NO.

DR	C. Smith	3-29-80
CHK	E. Cahill	4-4-80
APPD	W. White	4-9-80
APPROVED		
BY DIRECTION OF		

tau-tron inc Chelmsford, Ma. 01824

DRAWING TITLE: SCHEMATIC TRANSMIT GENERATOR PCB

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	[illegible]	57008	F

SCALE — | 552006 TX | SHEET 1 of 3

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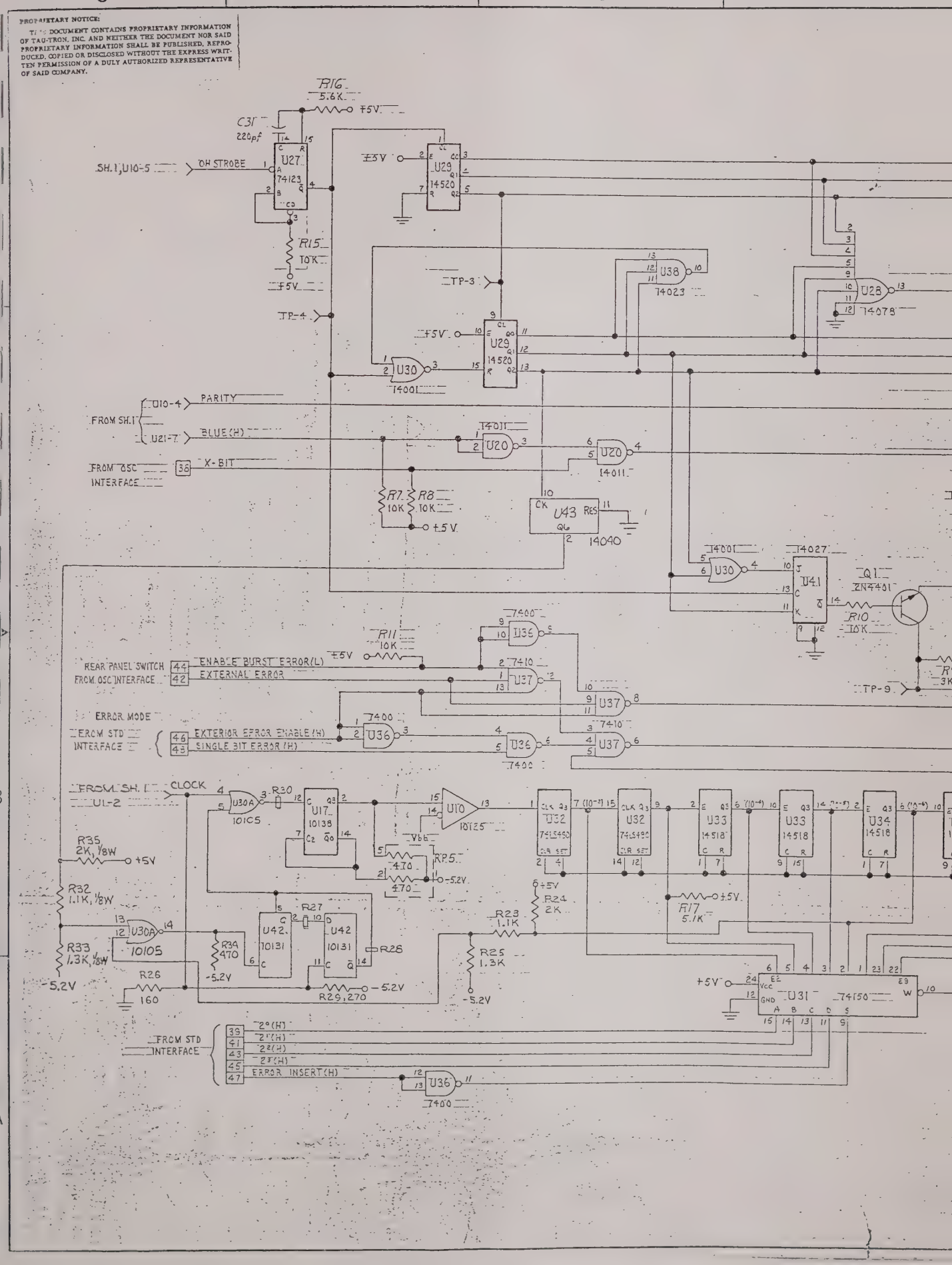
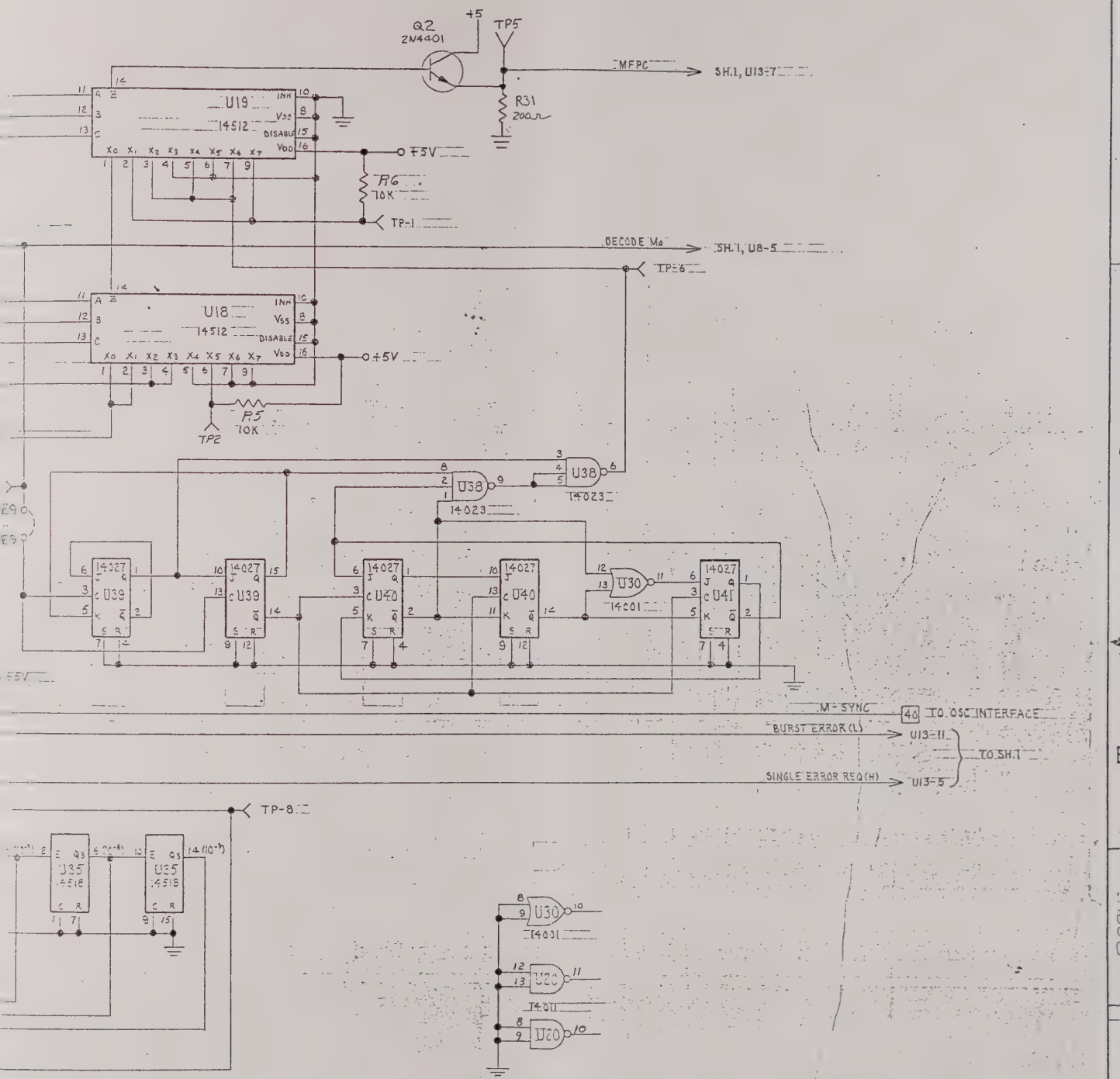


REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
SEE SH.1 FOR REVISIONS				



DEC. 16 '80

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±	CONTR NO.		tau-tron inc Chelmsford, Mass. 01824	
		DR C. Smith	3-29-80	DRAWING TITLE SCHEMATIC	
		CHK		TRANSMIT GENERATOR PCB	
		APPD	4-9-80		
TDK-57010	55200D TX	APPROVED		SIZE D	CODE IDENT NO.	DRAWING NO. 57008	REV E
NEXT ASSY	USED ON	BY DIRECTION OF		SCALE	55200D TX	SHEET 2 OF 2	
APPLICATION	MATERIAL:						

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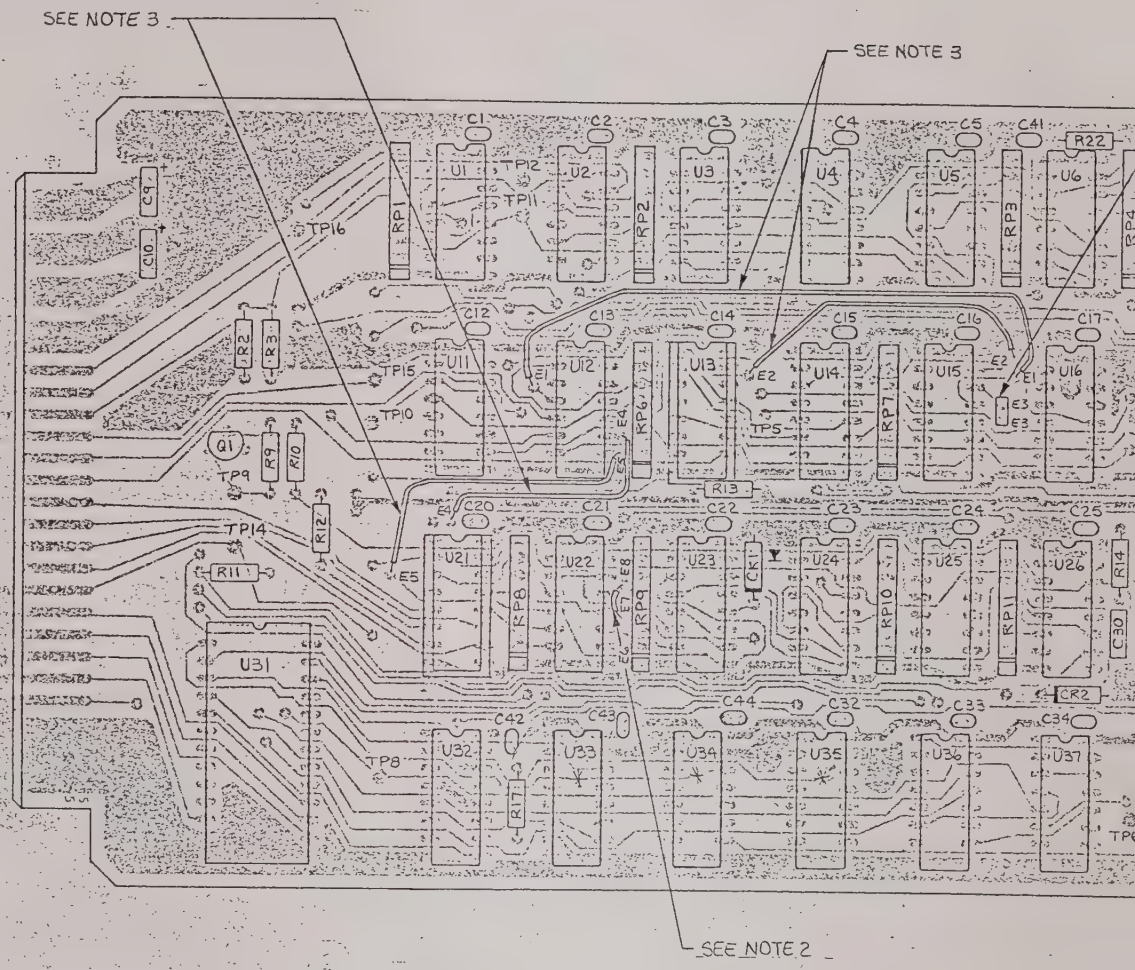
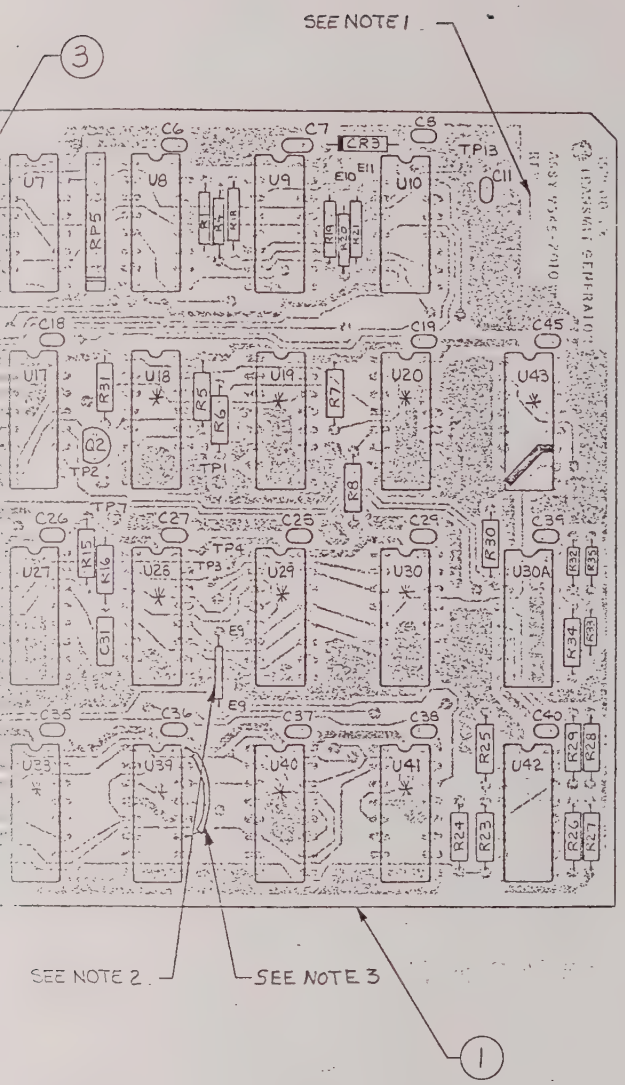


	H	REV PER ECO # 02033 TMM 1-24-85	1-25-85	KL
	J	UPDATED PER DOC ACCURACY PROG PER ECO # 02178 6-28-85 DH	7-29-85	MB
	K	UPDATED PER ECO # 02181 TB 8-8-85	8/19/85	KL
	L	REV PER ECO # 02656 MCD 10/27/86	10/29/86	PK



1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. JUMPERS TO BE #24 AWG BUS WIRE WITH SLEEVING
3. JUMPERS TO BE #30 AWG INSULATED WIRE
4. * - DENOTES STATIC SENSITIVE DEVICE

57010

DEC. 10. 80

J.T	12-31-80
B.COOPER	1-9-81
W. ROLLINS	1-20-81

ASSEMBLY
TRANSMIT GENERATOR PCB

S5200D TX

SEE P. L.
9545-7010

954 57010 L

S5200D TX

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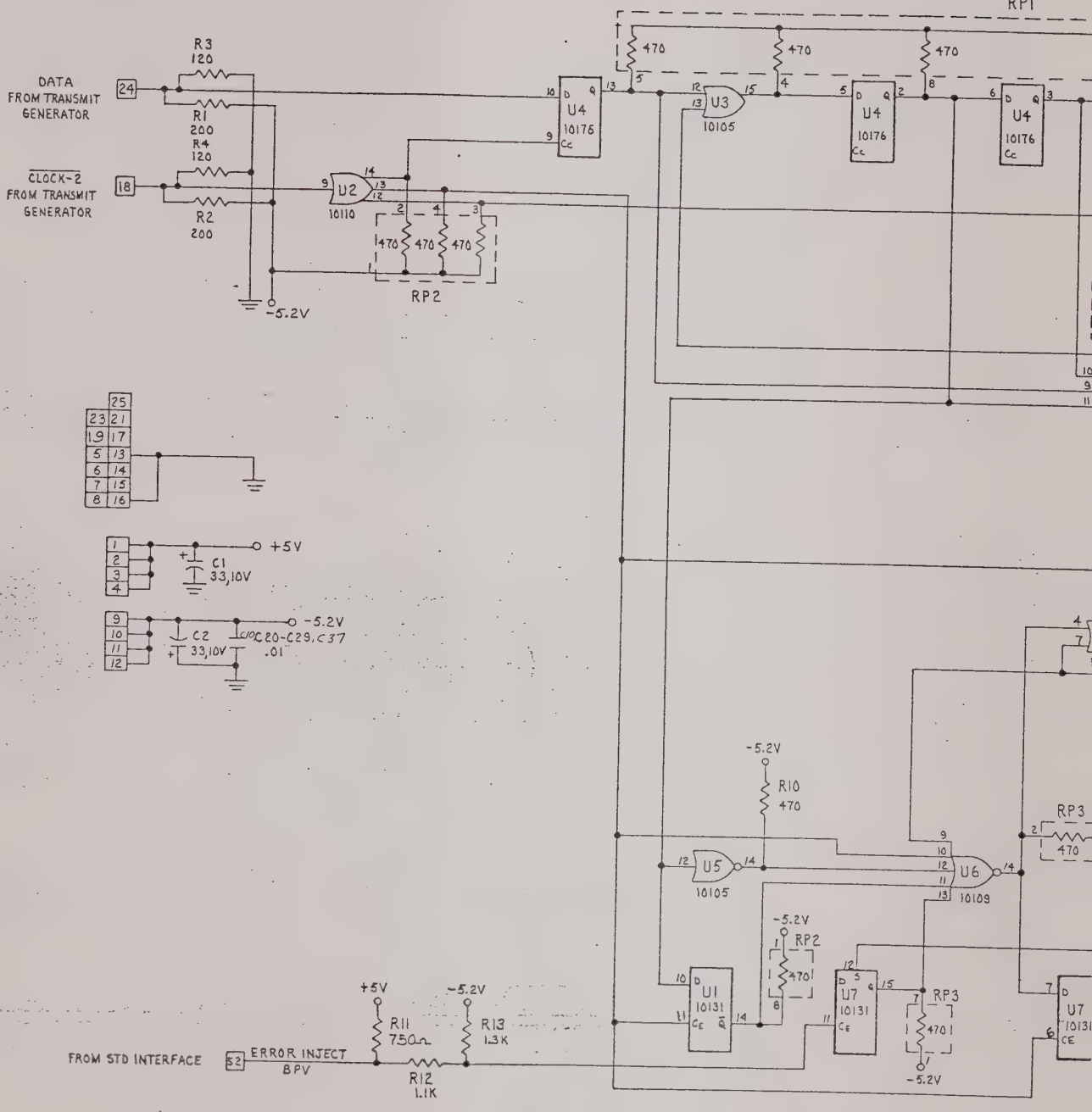

NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   A. RESISTANCE VALUES ARE IN OHMS 1/8 W ±5%.
   B. CAPACITANCE VALUES ARE IN MICROFARADS.
2. UNMARKED CAPACITORS ARE .01 μF DECOUPLING CAPS.
3. COMPONENTS TO BE CHANGE IN TEST AS REQUIRED, FOR DATA AND CLOCK OUTPUTS.

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	1	UPDATED 1/18/84 JG	1/18/84	JL
	A	REL TO PROD ECO# 01968 BWD 5 SEP 84	9/6/84	RL
	B	REV PER ECO# 02116 SMB 4/22/85	5/1/85	DK
	C	REV PER ECO# 02147 SMB 6/14/85	7/8/85	DK
	D	UPDATED PER DOC ACCURACY PROG PER ECO# 02178 7-11-85 JCH	7/24/85	DK
	E	REV PER ECO# 02470 TMM 5-2-86	5/5/86	DK
	F	REV PER ECO# 02409 RJA 5/21/86	6/16/86	RL

RP4 470 -5.2V
U3 10105
U4 10176 Cc
U9 10107
RP3 470
R6 470
U10 10109
-P RAIL
+P RAIL
TO SH. 2
R7 470
R5 470
U5 10105
U8 10135
R8 470
-N RAIL
+N RAIL
R9 470
RP2
BPV INSERT
R78 82.5 1% 1/4W
C38 1000 PF
R77 3.92 K 1% 1/4W
MPS2369 Q16
U14 10105
CLOCK I TO TX GENERATOR
R79 1.1K 1/4W
Y1 44.736 MHZ
C39 18PF NPO
R81 4.32K 1% 1/4W C40 .01
R82 392 1% 1/4W
R80, 2.43K 1% 1/4W
R62 500 CW
C45 8-18PF
C41
R85 392 1% 1/4W
Q17
R83 5.11K 1% 1/4W
MPS 2369
C42 47PF 39pf
C43 .01
C44 1000 PF
R86 5.11K 1% 1/4W
R84 392 1% 1/4W
L3 .390 μH

REFERENCE DESIGNATIONS	
USED	UNUSED
K3	C47
T2	C48
U14	C49
Y1	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

CONTR NO.

APPD		
DR	Bryant M Beverlin	7-26-83
CHK		8-1-83
		8-1-83
		9-19-83
		10-13-83

APPROVED
BY DIRECTION OF S A McLaine 9/19/84

NEXT ASSY S5250
USED ON
APPLICATION

DEC 16 '86

tau-tron inc Chelmsford, Mass. 01824

DRAWING TITLE
SCHEMATIC
OUTPUT. AMPLIFIER / OSC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D		57643	F

SCALE | S5250 | SHEET 1 OF 2

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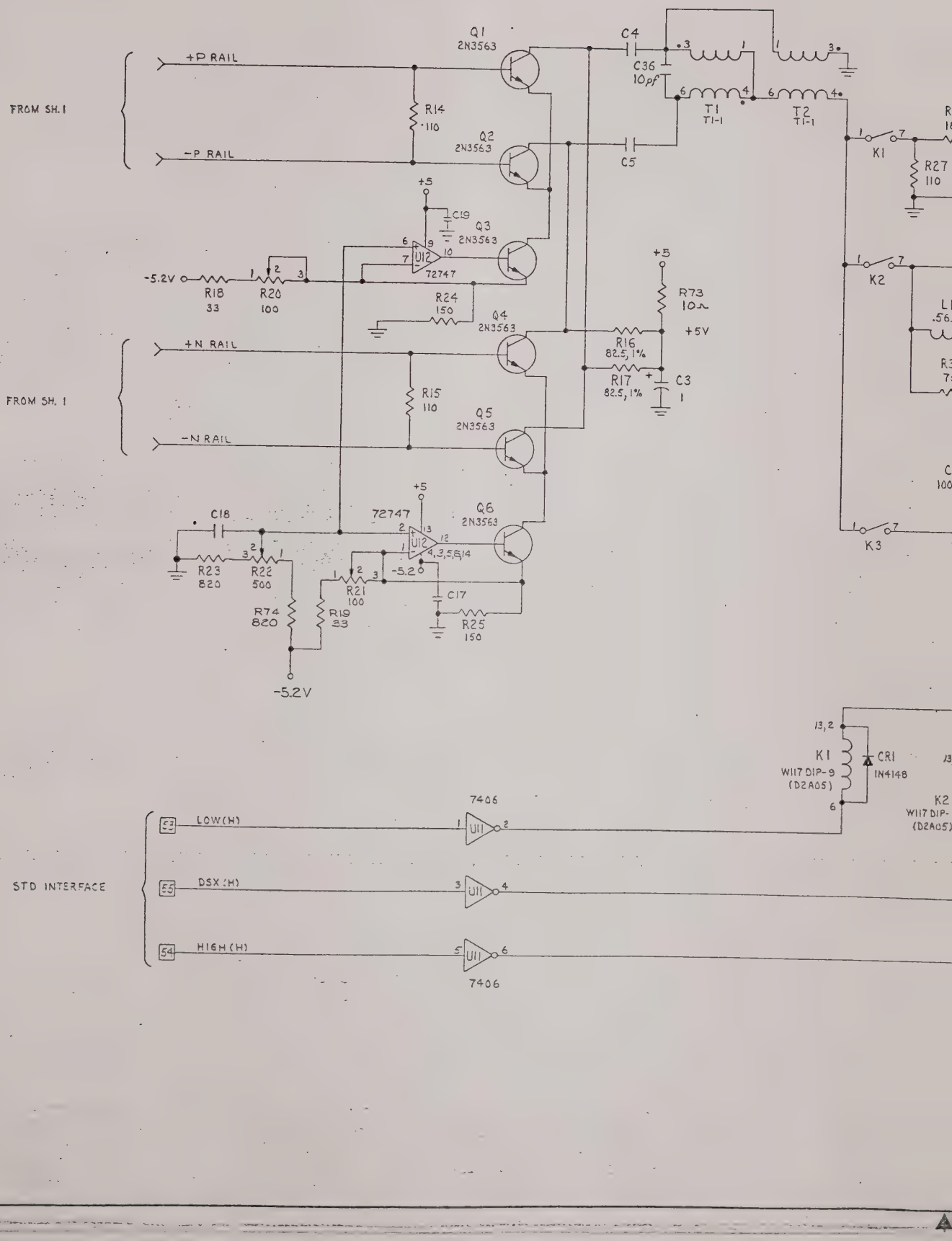
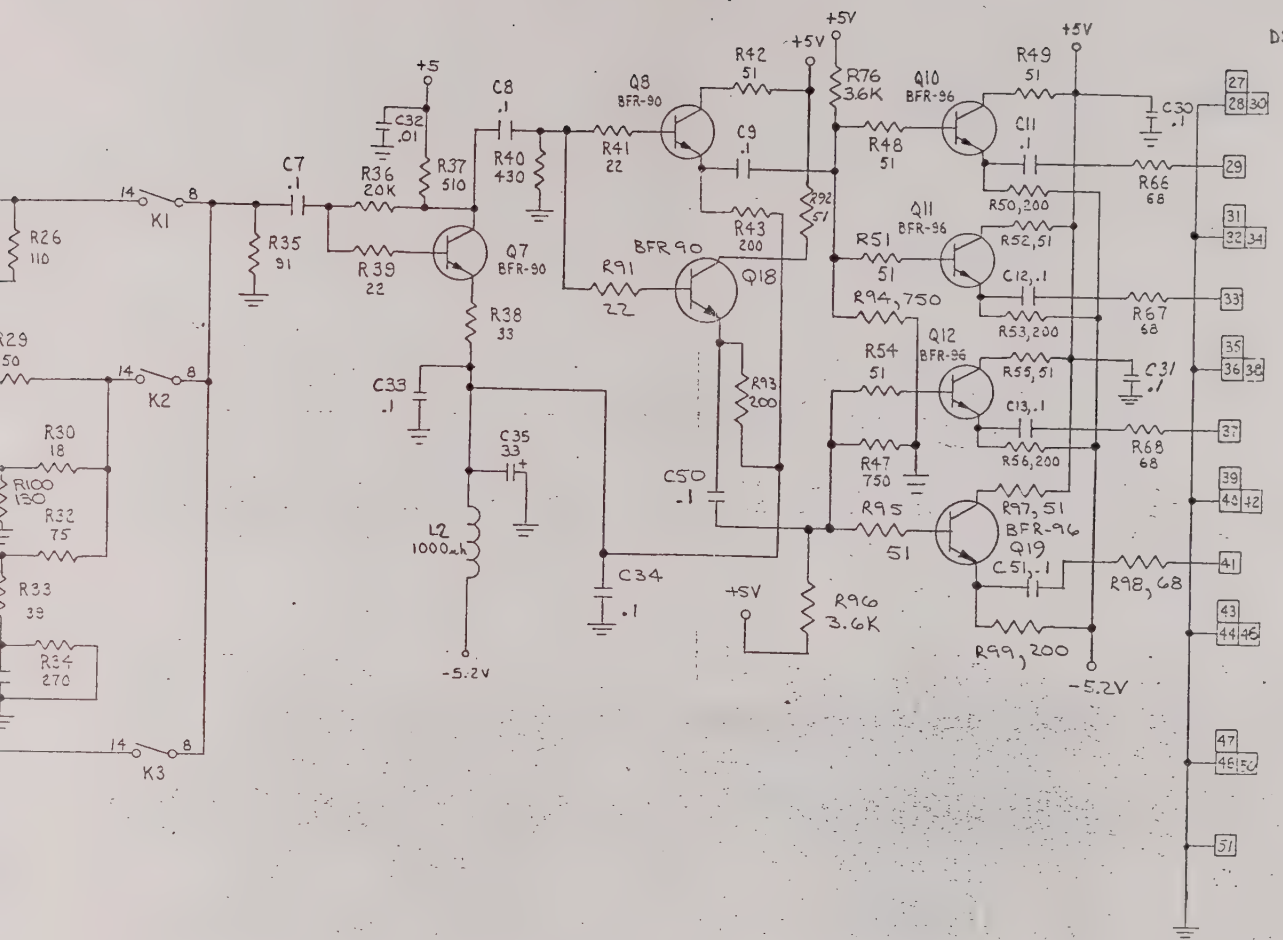
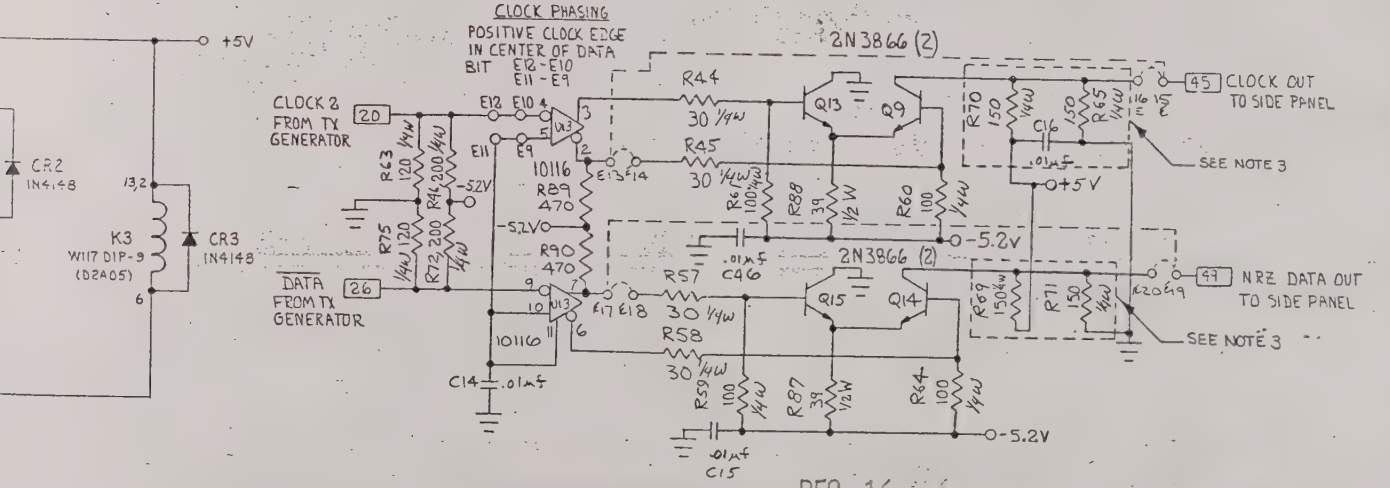


REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
		SEE SHEET 1 FOR REVISIONS		





WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	CONTR NO. DEO. 14	tau-tron inc Chelmsford, Ma. 01824		
	TOLERANCES: ANGLES ±	DR Bryant M Bucelli 7-26-83	DRAWING TITLE		
	FRACTIONS ±	CHK [illegible] 8-1-83	SCHEMATIC		
	3 PLACE DECIMALS ±	APPD Alex Sargent 8-1-83	OUTPUT AMPLIFIER/OSC		
	2 PLACE DECIMALS ±	APPD Rotella 9-19-84			
	1 PLACE DECIMALS ±	APPD [illegible] 9-19-84			
55250	MATERIAL:	APPROVED	SIZE D	CODE IDENT NO.	DRAWING NO. 57643 REV F
NEXT ASSY / USED ON		BY DIRECTION OF SA Mulcahy 9/19/84	SCALE	55250	SHEET 2 OF 2
APPLICATION					

57643

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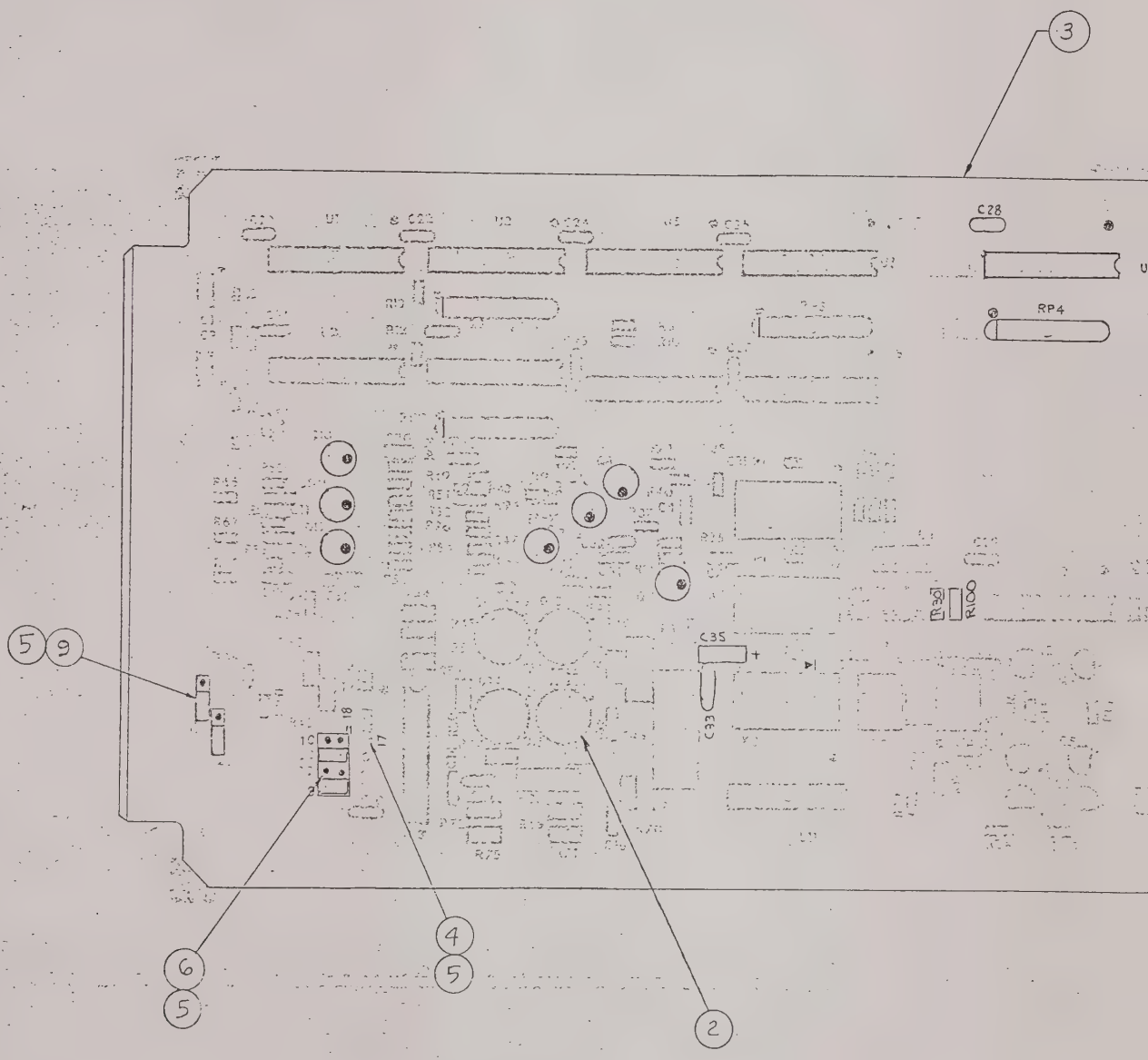
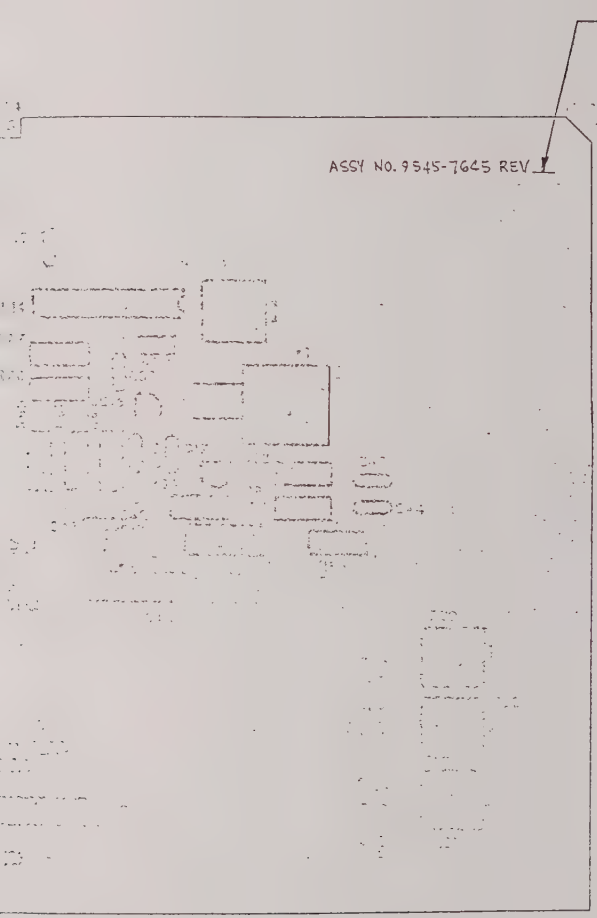


	2	UPDATED 5-31-84 L. Rankin	5-31-84	
	3	UPDATED L. Rankin 8-13-84	8-14-84	
	4	SWAPPED C32 & C35 BUDD 5 SEP 84		
	A	REL. TO PROD. ECO # 01968 BUDD 5 SEP 84	9/6/84	
	B	REV PER ECO # 02116 dwg 4/22/85	5/1/85	PK
	C	UPDATED PER DOC ACCURACY PROG PER ECO # 02178 7-1-85 DN	7/23/85	PK
	D	REV PER ECO # 02409 BTA 5/21/86	6-16-86	



NOTES:
1. PLACE THE ASSEMBLY PARTS LIST REVISION MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

DEC. 16 '86

	Bryant M. Bevelle 9-13-83	ASSEMBLY OUTPUT AMP PCB S5250
	BUDD 2-8-84	
	A.J. SENECAL 9-14-83	
	1-19-84	
	9-19-84	
S5250	SEE P/L	D 57645 D
	9545-7645	S.R. McComb 9/19/84 SIZE S5250 SHEET 1 OF 1

57645

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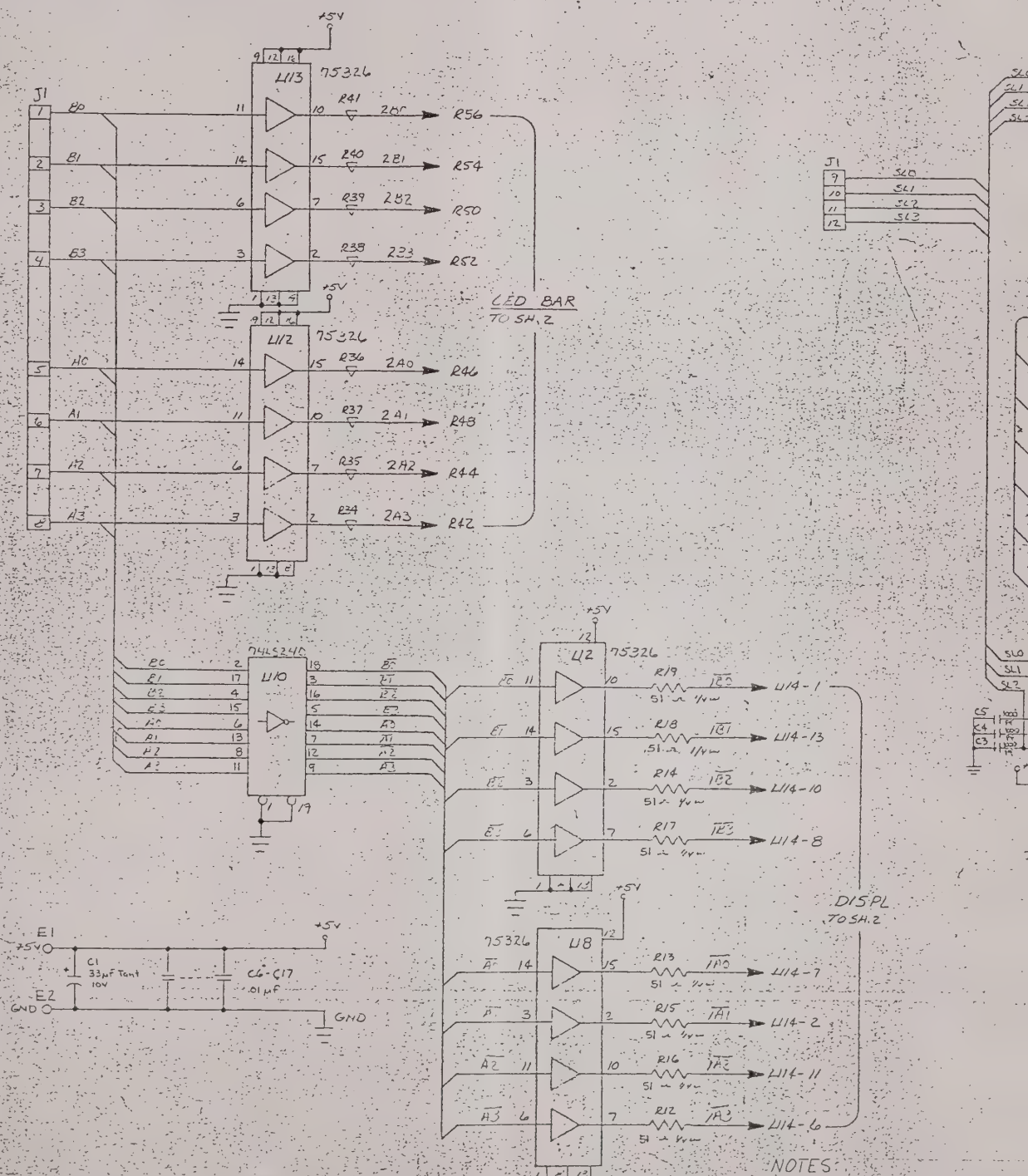
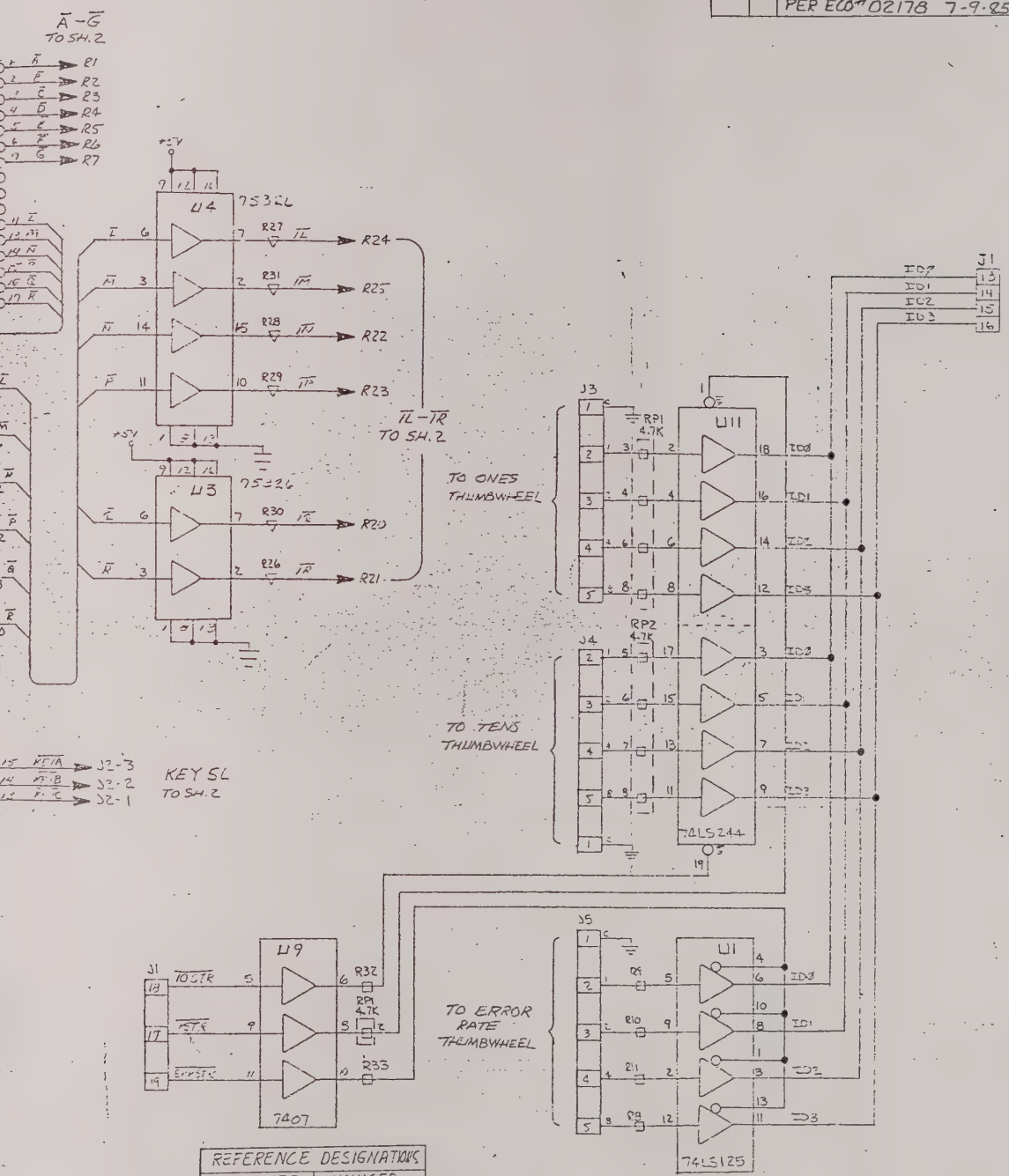


NOTES:

1. UNLESS OTHERWISE SPECIFIE
   A. RESISTANCE VALUES ARE IN OHM
   B. CAPACITANCE VALUES ARE IN M
2. ▽ DENOTES 150 OHM 1/4 W, 5
   ▭ DENOTES 4.7K OHM 1/4 W, 5

ZONE	LTR	DESCRIPTION	DATE	APPROVED
	1	UPDATED ORIGINAL BUDD 18 NOV 83	11/21/83	RL
	2	REVISED PER PROTOTYPE BUDD 29 DEC 83	1/10/84	RL
	3	REF DWG 2228 (LIVE) PJL 23 MAY 84	3/20/84	RL
	A	REV PER ECO 00815 BUDD 27 JUN 84	7-6-84	RL JPE
	B	UPDATED PER DOC ACCURACY PROG PER ECO# 02178 7-9-85 DH	7/24/85	PR



REFERENCE DESIGNATIONS	
LAST USED	UNUSED
C17	E2
Q21	J5
R56	U20
RP2	Z30

DEC. 16, '85

1/4W, 5%.
ROHMS.
RESISTOR TO +5V.
RESISTOR TO +5V.

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±

MATERIAL:

D37642	S5250
NEXT ASSY	USED ON
APPLICATION	

CONTR NO.

DR [illegible] 7-1-83
CHK [illegible] 8/2/83
APPD [illegible] 8-24-83

APPROVED

BY DIRECTION OF

tau-tron INC Chelmsford, Mass. 01824

DRAWING TITLE
SCHEMATIC
S5250
FRONT PANEL BOARD

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	[illegible]	57640	B

SCALE [illegible] S5250 SHEET 1 OF 2

57640

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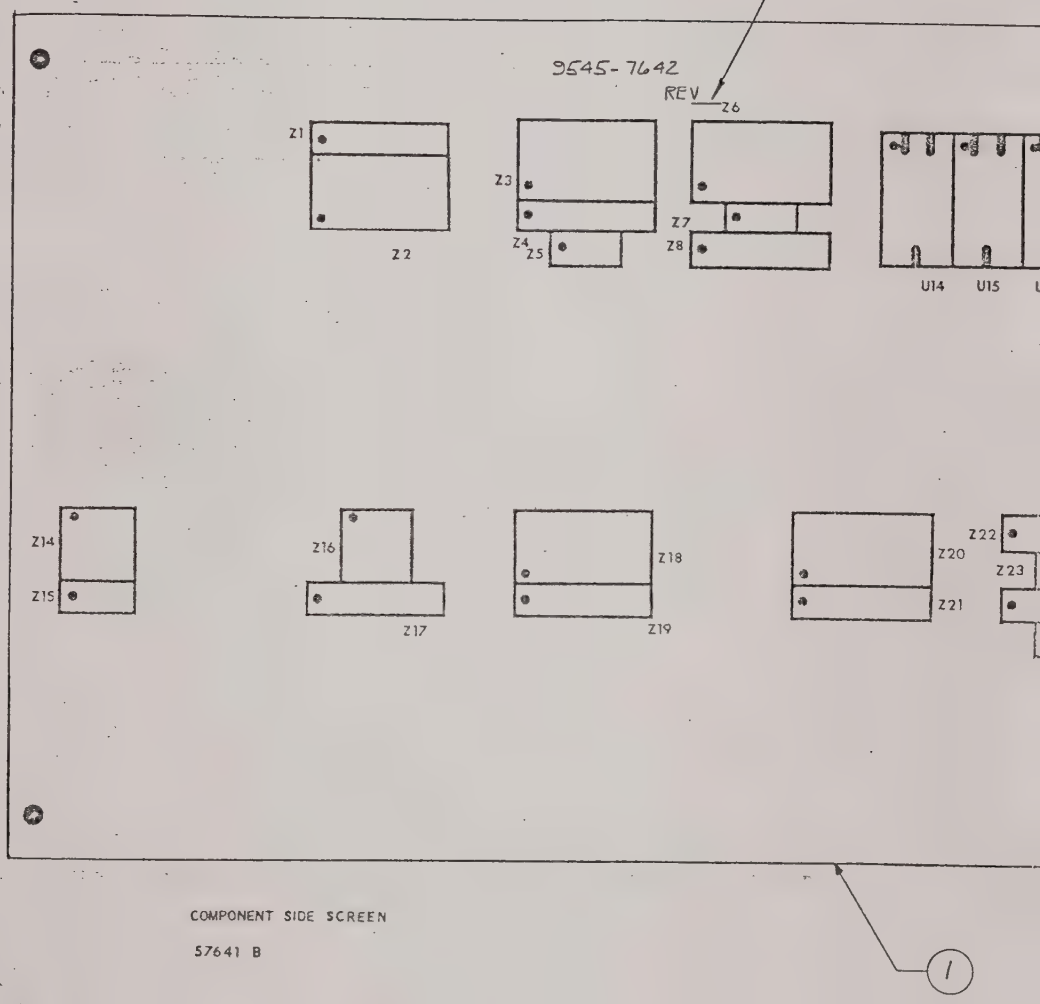
SEE NOTE 1.

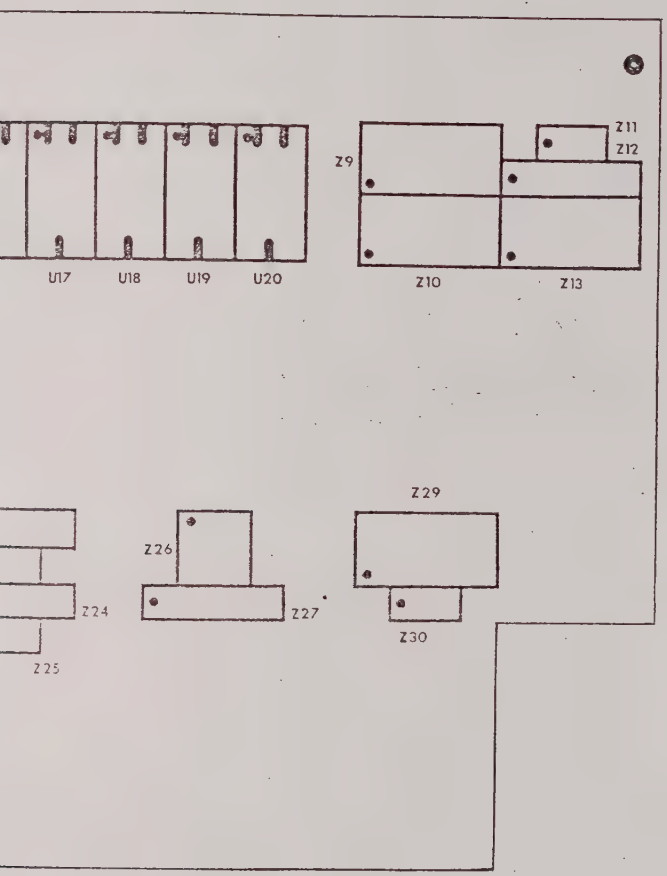


COMPONENT SIDE SCREEN

57641 B

1

ZONE	LTR	DESCRIPTION	DATE	APPD
	A	[illegible] PER ECO# 1915 TMM 7-2-84	7-6-84	6/8/84
	B	UPDATED PER DOC ACCURACY PROG PER ECO# 02178 6-28-85 ⓓ IT	7/23/85	FK JK



NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

DEC. 16. [illegible]

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	CONTR NO	tau-tron INC Chelmsford MA 01824
	TOLERANCES ANGLES ± 1°	DR BMB 8-26-83	
	FRACTIONS ± 1/64	CHK RJL 8-31-83	DRAWING TITLE ASSEMBLY FRONT PANEL PCB S5250
	3 PLACE DECIMALS ± .005	APPD AS. 8-31-83	
	2 PLACE DECIMALS ± .01		
	1 PLACE DECIMALS ± .1		
S5250	MATERIAL	APPROVED	SIZE D | CODE IDENT NO 954 | DRAWING NO 57642 | REV B
NEXT ASSY | USED ON		BY DIRECTION OF	SCALE 2/1 | S5250 | SHEET 1 OF 2
APPLICATION			

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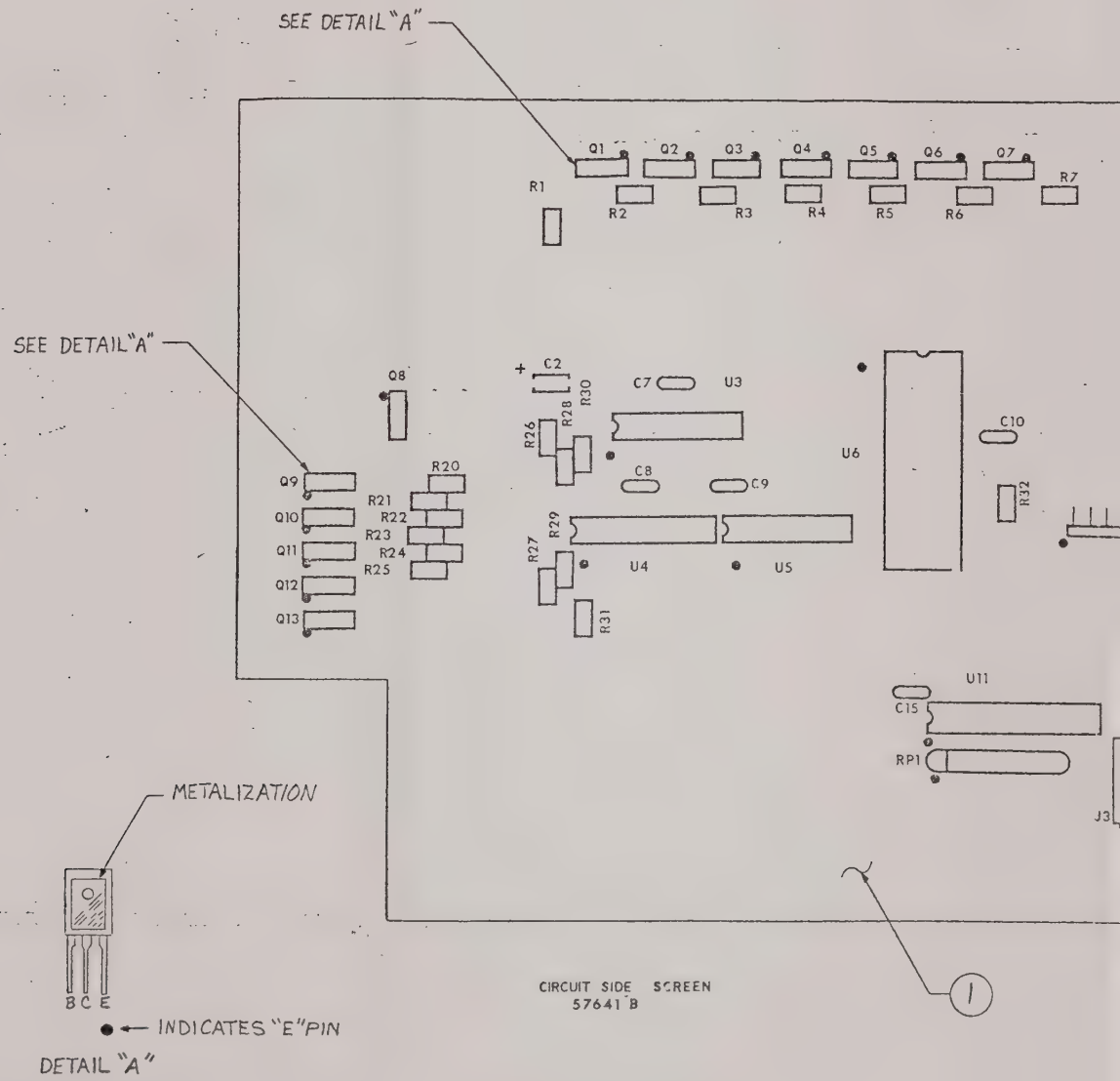
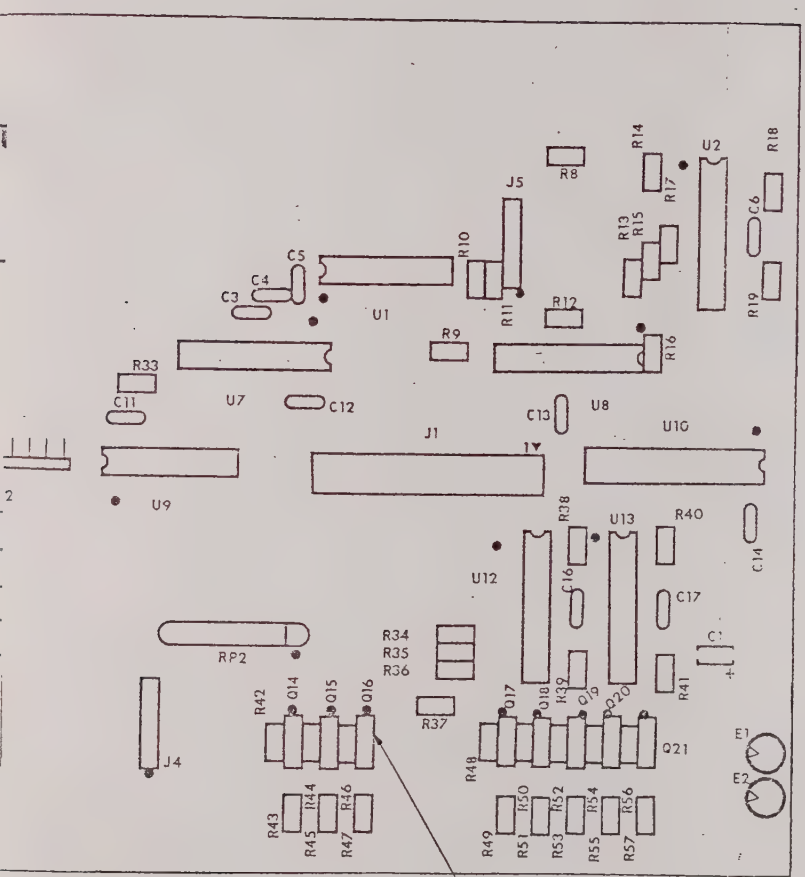


		SEE SHEET 1 FOR REVISION		



DEC. 16, 1

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±	CONTR NO	tau-tron inc Chelmsford, MA 01824
		DR J. MILLEY 1-11-84	DRAWING TITLE ASSEMBLY FRONT PANEL P.C.B.
S5250	MATERIAL	CHK	
NEXT ASSY USED ON		APPROVED	SIZE D CODE IDENT NO 954 DRAWING NO 57642 REV B
APPLICATION		BY DIRECTION OF	SCALE 2/1 S5250 SHEET 2 OF 2

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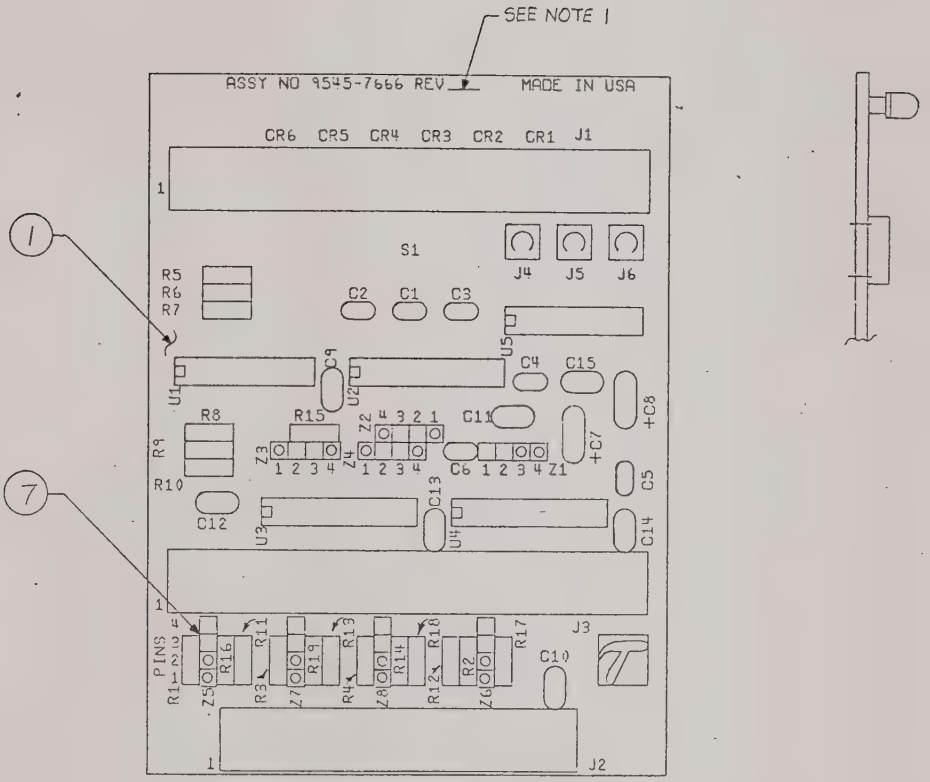
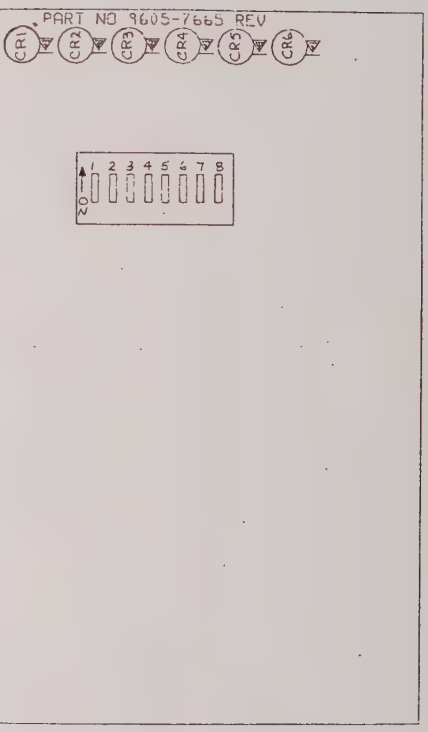


PATCH TABLE

PATCH	RS232	RS449/422	RS449/423
Z1	1-2	1-2	2-3
Z2	2-3	2-3	1-2
Z3	2-3	2-3	1-2
Z4	2-3	2-3	1-2
Z5	3-4	2-3	3-4
Z6	3-4	2-3	3-4
Z7	3-4	2-3	3-4
Z8	3-4	2-3	3-4

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	2	UPDATED THM 7-25-84	8-24-84	
	3	UPDATED RSR 8-23-84	8-24-84	
	A	RELEASED PER ECO #01990 THM 10-4-84	10-10-84	
	B	UPDATED PER DOC ACCURACY PROG PER ECO #02178 7-1-85 DH	7/23/85	PK



NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

DEC

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± ~ FRACTIONS ± ~ 3 PLACE DECIMALS ± ~ 2 PLACE DECIMALS ± ~ 1 PLACE DECIMALS ± ~ MATERIAL: ~	CONTR NO. DEC. ~~	tau-tron INC Chelmsford, MA 01824
		DRED MENDEZ 3-21-84	DRAWING TITLE ASSEMBLY RS-232 RS-449 INTERFACE BD.
		CHK [signature] 10-13-84	
		APPD [signature] 10-10-84	
		[signature] [illegible]	
57680-81 / S5250		APPROVED [signature]	SIZE D / CODE IDENT NO. 954 / DRAWING NO. 57666 / REV B
NEXT ASSY / USED ON		BY DIRECTION OF	
APPLICATION			SCALE 2/1 / S5250 / SHEET 1 OF 1

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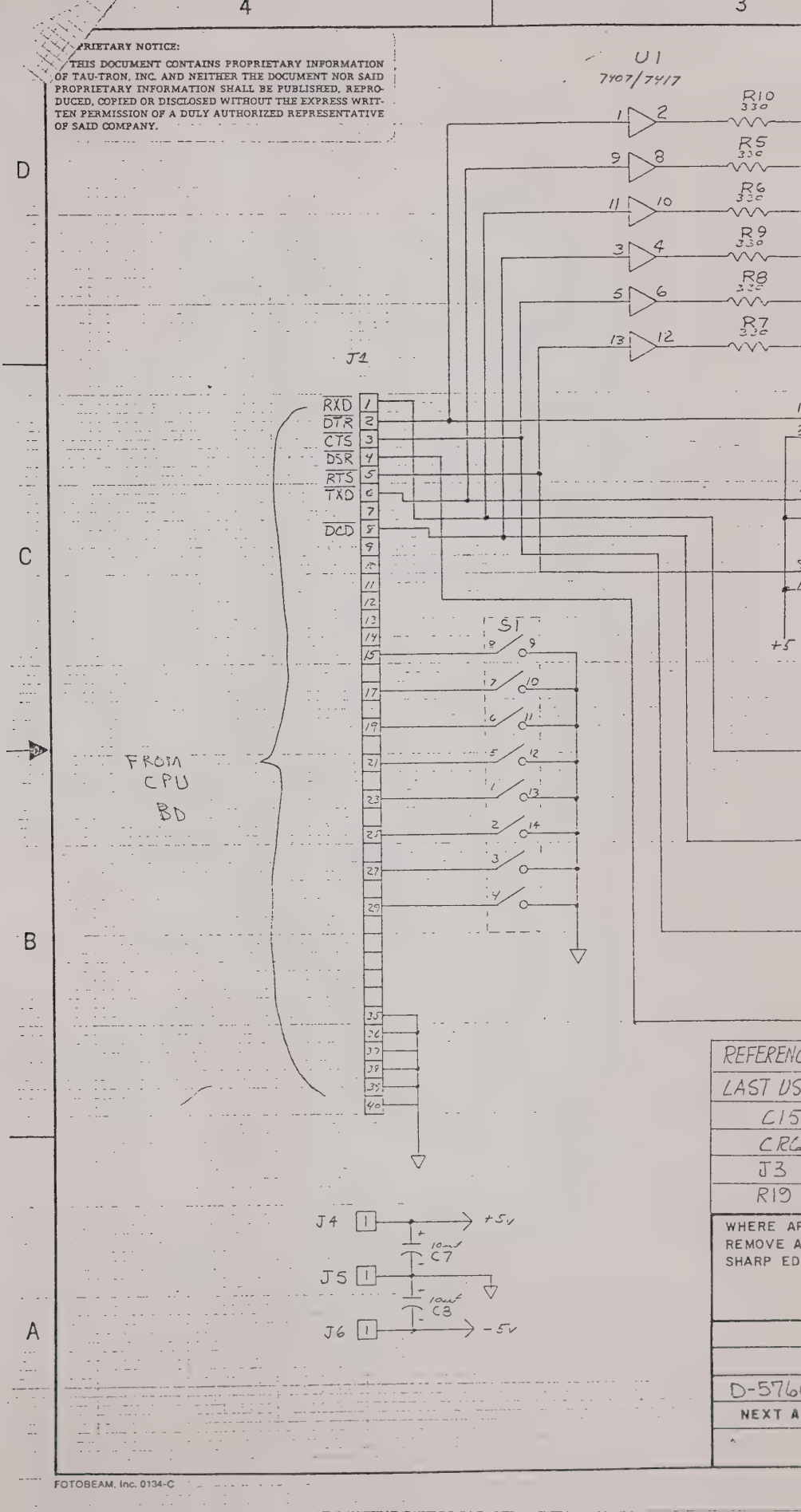
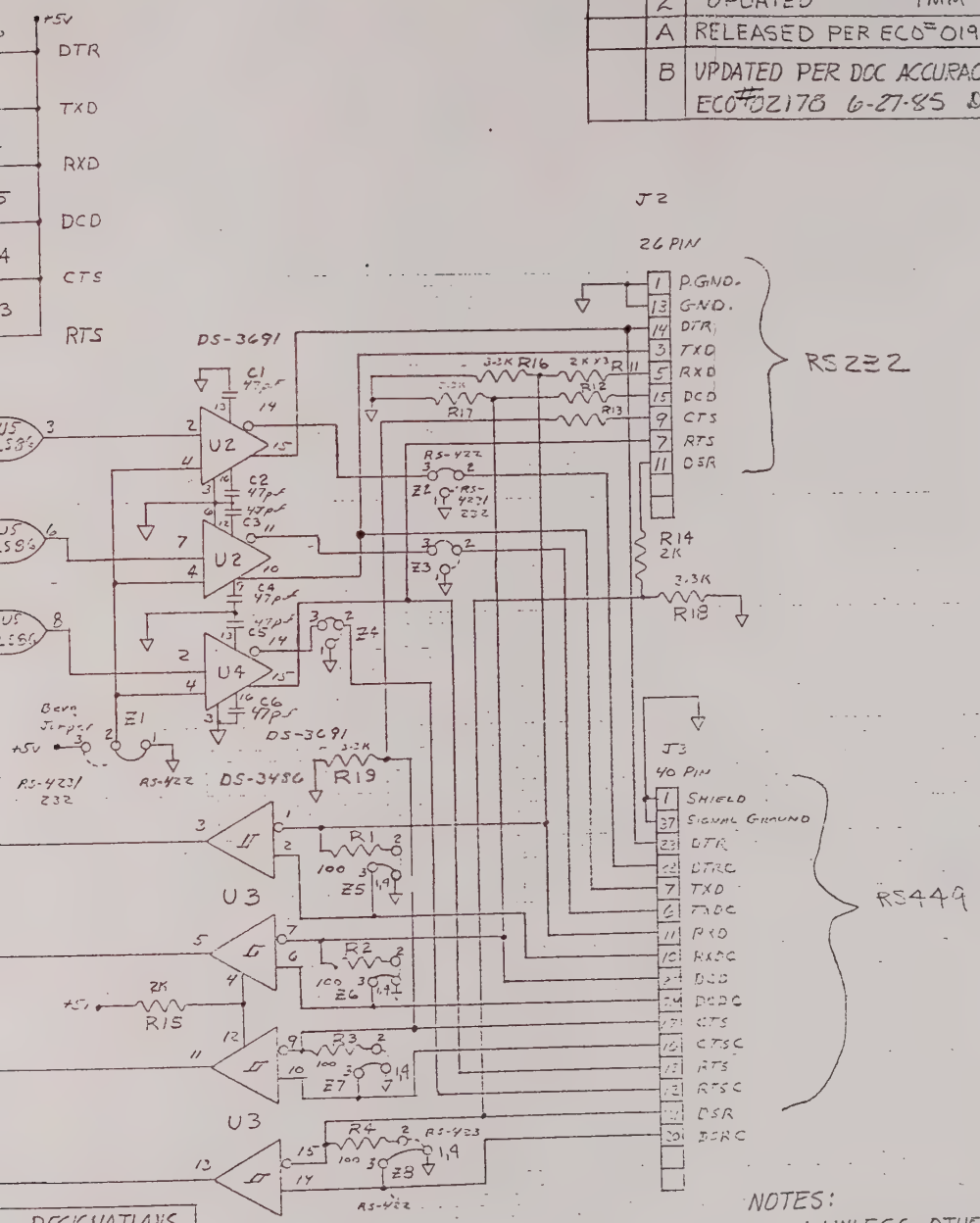


REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	2	UPDATED TMM 7-20-84	8/4/84	
	A	RELEASED PER ECO#01990 TMM 10-4-84	10-10-84	
	B	UPDATED PER DOC ACCURACY PROG PER ECO#02178 6-27-85 DH	7/23/85	



NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   A. RESISTANCE VALUES ARE IN OHMS, 1/4W, ±5%.
   B. CAPACITANCE VALUES ARE IN MICROFARADS.

DESIGNATIONS	
	LAST USED
	S1
	U15
	Z8

	S5250
SY	USED ON
PPLICATION	

LICABLE:
L BURRS & BREAK
S TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± 4
FRACTIONS ± 4
3 PLACE DECIMALS ± 4
2 PLACE DECIMALS ± 4
1 PLACE DECIMALS ± 4

MATERIAL: 4

CONTR NO.

DR	S.R. Mitchell	3/1/84
CHK	E. Estrella	10-10-84
APPD		10.10.84
		10-10-84

APPROVED S.R. Mitchell 10/10/84
BY DIRECTION OF

tau-tron inc
Chelmsford, Ma. 01824

DRAWING TITLE
ECO. 10: 00
RS-232/RS-449 INTERFACE BD.

SIZE	CODE IDENT NO.	DRAWING NO.	REV
C	4	57664	B

SCALE 4 SHEET 1 of 1

57664

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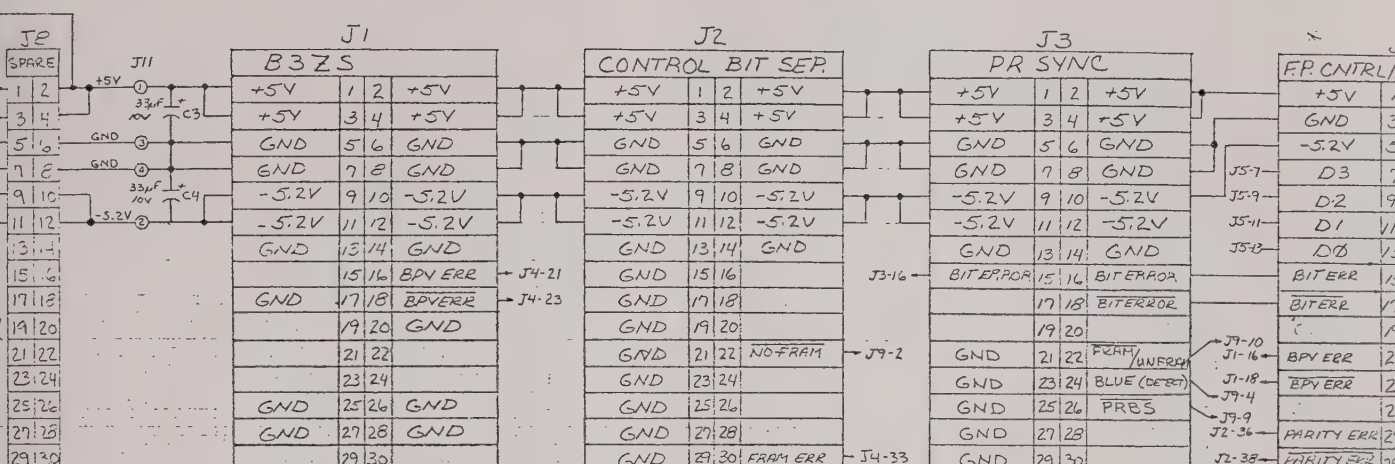
→ J9-6 & 8 :



J9

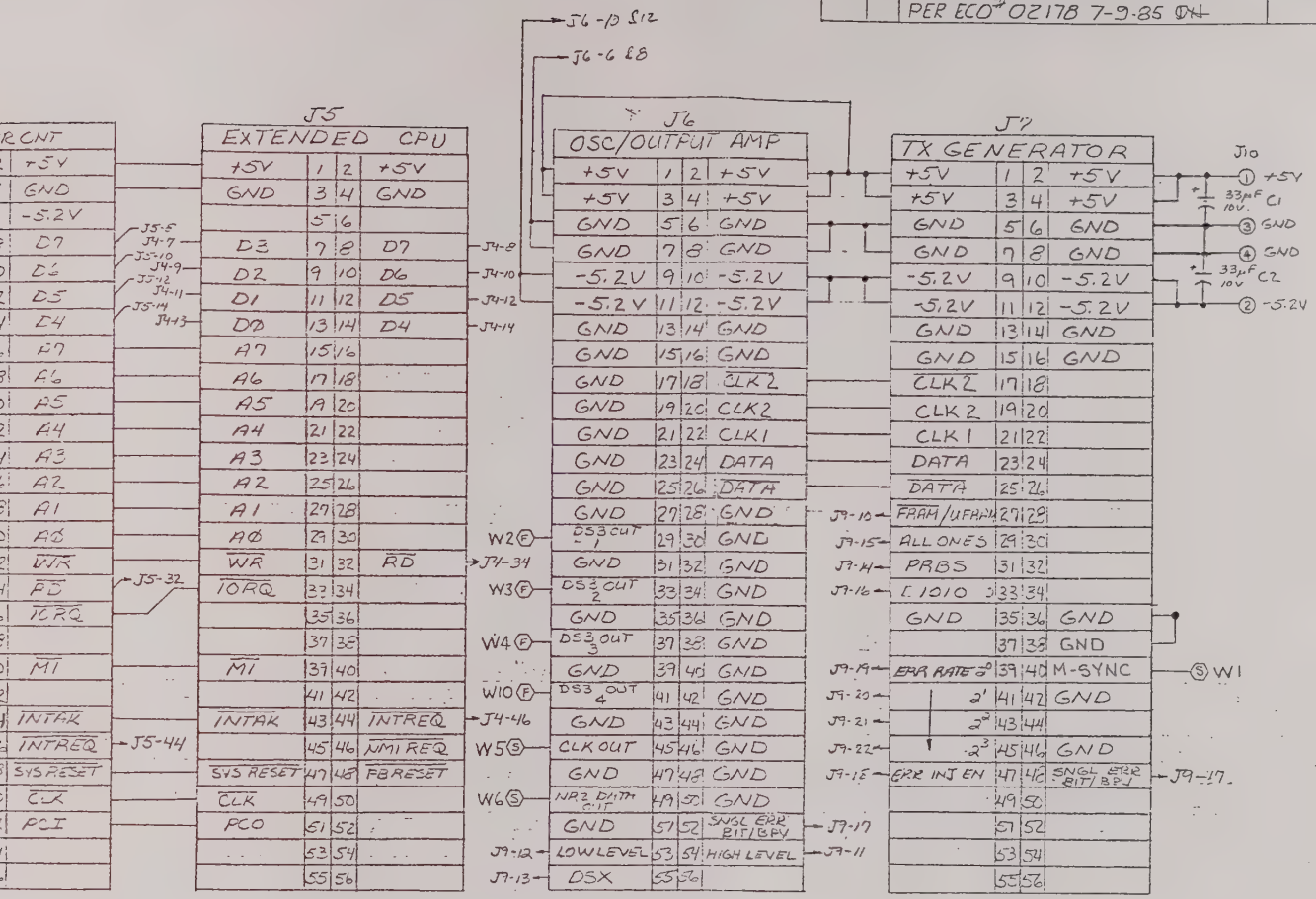
J1-56	NO SIG	1	2	NO FRAME	
J3-44	NO SYNC	3	4	BLUE	
J1-52	CONTROL B	5	6	CONTROL A	
J3-56	ALL ONES	7	8	1010	
J3-26	PRBS	9	10	FRAM/UNFRAM	
J6-54	HIGH LEVEL	11	12	LOW LEVEL	
J6-55	DSX	13	14	PRBS	
J7-29	ALL ONES	15	16	1010	
J7-48 + J6-52	SNGL ERR BIT/BPV	17	18	ERROR INJ ENABLE	
J7-39	ERR RATE 2^0	19	20	ERR RATE 2^1	
J7-43	ERR RATE 2^2	21	22	ERR RATE 2^3	
		23	24		
		25	26		

Ⓕ = Coax to Front Panel

Ⓢ = Coax to side panel

NOTE: ALL COAX HARD SOLDERED TO P.C. BOARD

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	A	REL PER ECO# 01915 M.E.H 7-6-84	7-6-84	
	B	REV PER ECO# 01934 M.E.H 8-15-84	8-22-84	
	C	UPDATED PER DOC ACCURACY PROG PER ECO# 02178 7-9-85 DH	7/24/13	



DEC. 16. '86

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±	CONTR NO.	tau-tron INC Chelmsford, Mass. 01824	
		DR 7-7-83	DRAWING TITLE	
		CHK 8-4-83	S5250 MOTHER BAD	
		APPD 7/24/87		
5250	MATERIAL:	APPROVED	SIZE D	CODE IDENT NO.	DRAWING NO. 57637	REV C
NEXT ASSY / USED ON		BY DIRECTION OF	SCALE	SHEET 1 OF 1
APPLICATION				

REV 1 12-21-83

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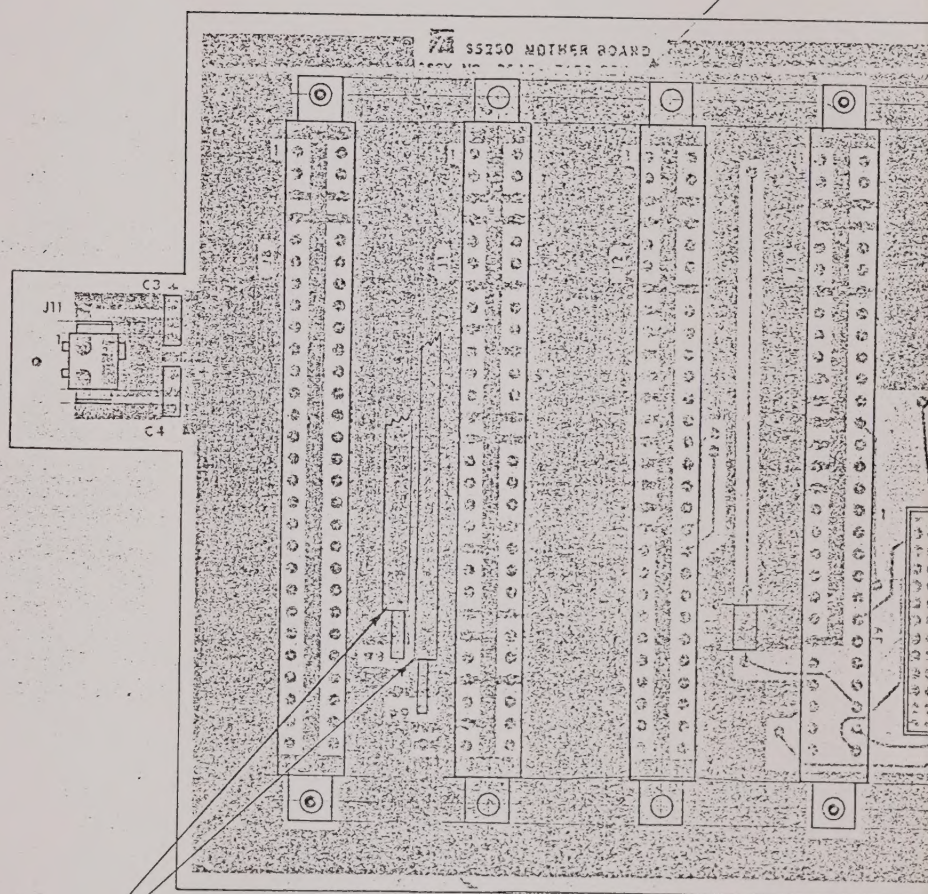
SEE NOTE

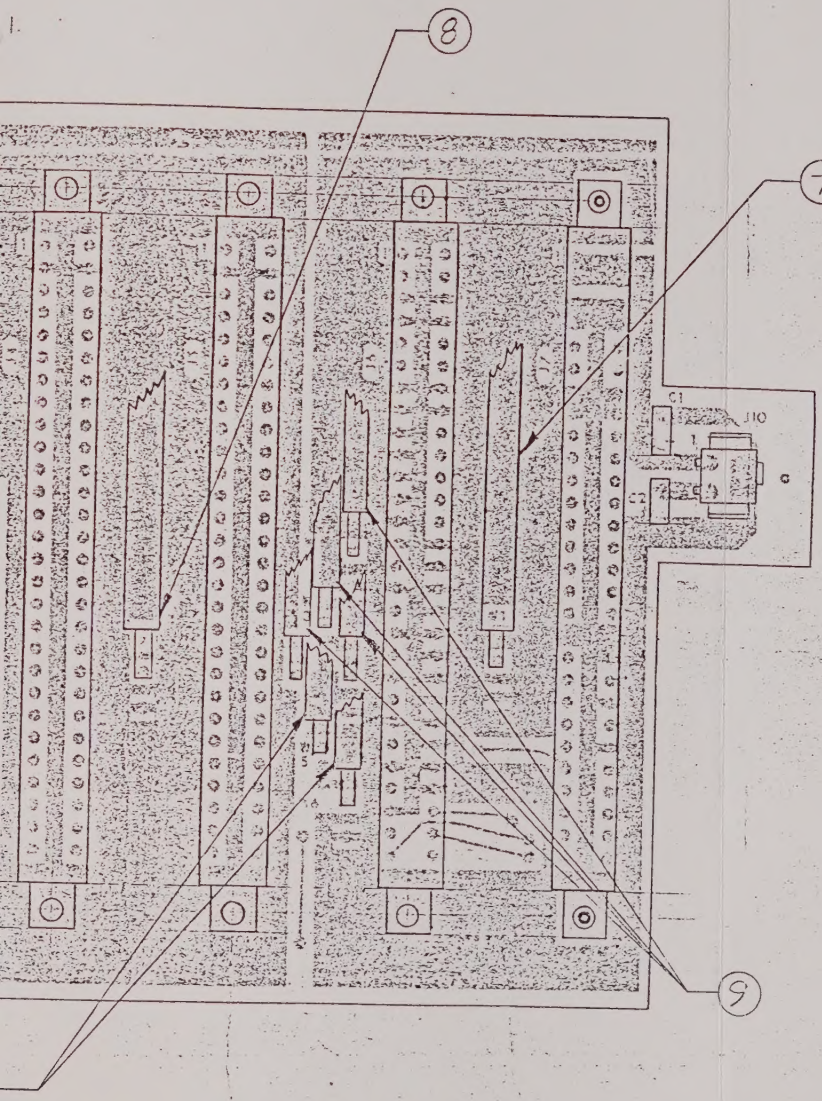


1 ~~PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.~~

NOTES:

NOTES:

	A	REL PER ECO # 01815 MCH 7-6-82		KE/JK
	B	REVERSED PART ECO # 01954 EJ 8-19-83	9-27-83	
	C	UPDATED PER DOCUMENT CONTROL PROC PER ECO # 02173 10-23-85 CH	7/23/85	PK
	D	REV PER ECO # 02328 MCD 7/2/86	7/11/86	PK
	E	REV PER ECO # 02580 MCD 8/14/86	8/18/86	PK



DEC. 16, '86

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO .1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES ANGLES ±
FRACTIONS ±
2 PLACE DECIMALS ±
3 PLACE DECIMALS ±
4 PLACE DECIMALS ±
MATERIAL

CONTR NO

DR	BJW	8-19-82
CHK	RDL	8-19-82
APP	AS	8-19-82
APPROVED		
DT MFR/QUALN OF		

tau-tron

DRAWING TITLE: ASSEMBLY - MOTHER BOARD PCB

D57624	S5250
NEXT ASSY	USED ON
APPLICATION	

SIZE	CODE IDENT NO	DRAWING NO	REV
D		57639	E

SCALE 2/1 | S250 | SHEET 1 OF 1

10 Lyberty Way, Westford, MA 01886
(617) 692-5100 Telex 750245 1-800-TAU-TRON (Outside MA)